ONE MARK

TWO MARKS

GATE SOLVED PAPER - EC

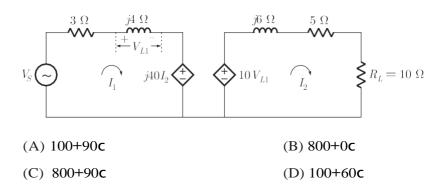
NETWORK ANALYSIS

2013 Consider a delta connection of resistors and its equivalent star connection as Q. 1 shown below. If all elements of the delta connection are scaled by a factor k, k > 0, the elements of the corresponding star equivalent will be scaled by a factor of R_{-} R_{i} $\gtrsim R_{\epsilon}$ R_A (A) k^2 (B) *k* $(C) \ 1/k$ (D) \sqrt{k} The transfer function $\frac{V_2 \wedge s h}{V_1 \wedge s h}$ of the circuit shown below is Q. 2 100 μF $10 \ \mathrm{k}\Omega$ $V_1(s)$ $100 \ \mu F$: (A) $\frac{0.5s+1}{s+1}$ (B) $\frac{3s+6}{s+2}$ (C) $\frac{s+2}{s+1}$ (D) $\frac{s+1}{s+2}$ A source $v_s \wedge t h = V \cos 100 pt$ has an internal impedance of $\wedge 4 + j3hW$. If a purely Q. 3 resistive load connected to this source has to extract the maximum power out of the source, its value in W should be (A) 3 (B) 4 (C) 5 (D) 7

Q. 4

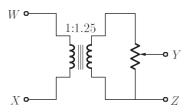
2013

then the In the circuit shown below, if the source voltage $V_s = 100+53.13$ c V Thevenin's equivalent voltage in Volts as seen by the load resistance R_L is



The following arrangement consists of an ideal transformer and an attenuator

which attenuates by a factor of 0.8. An ac voltage $V_{WX1} = 100$ V is applied across WX to get an open circuit voltage V_{YZ1} across YZ. Next, an ac voltage V_{YZ2} = 100 V is applied across YZ to get an open circuit voltage V_{WX2} across WX. Then, V_{YZ1} / V_{WX1} , V_{WX2} / V_{YZ2} are respectively,

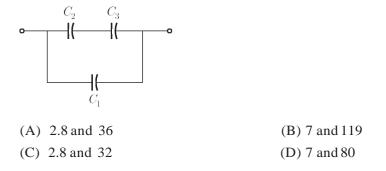


(A) 125 / 100 and 80/100 (C) 100 / 100 and 100 / 100 (B) 100 / 100 and 80 / 100 (D) 80 / 100 and 80 / 100

Two magnetically uncoupled inductive coils have Q factors q_1 and q_2 at the chosen operating frequency. Their respective resistances are R_1 and R_2 . When connected in series, their effective Q factor at the same operating frequency is (A) $q_1 + q_2$

(B) $^{1} / q_{1}h + ^{1} / q_{2}h$ (C) $^{q_1R_1}+q_2R_2h/^{R_1}+R_2h$ (D) $^{q_1R_2}+q_2R_1h/^{R_1}+R_2h$

Three capacitors C_1 , C_2 and C_3 whose values are 10 mF, 5 mF, and 2 mF respectively, have breakdown voltages of 10 V, 5 V and 2 V respectively. For the interconnection shown below, the maximum safe voltage in Volts that can be applied across the combination, and the corresponding total charge in mC stored in the effective capacitance across the terminals are respectively,

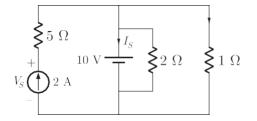


Q. 6

Q. 7

Common Data For Q. 8 and 9:

Consider the following figure



- **Q.8** The current I_S in Amps in the voltage source, and voltage V_S in Volts across the current source respectively, are
 - (A) 13, -20
 - (B) 8, 10
 - (C) 8,20
 - (D) −13,20

Q. 9 The current in the 1W resistor in Amps is

- (A) 2
- (B) 3.33
- (C) 10
- (D) 12

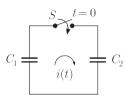
2012

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Q. 10

Q. 11

In the following figure, C_1 and C_2 are ideal capacitors. C_1 has been charged to 12 V before the ideal switch S is closed at t = 0. The current i(t) for all t is



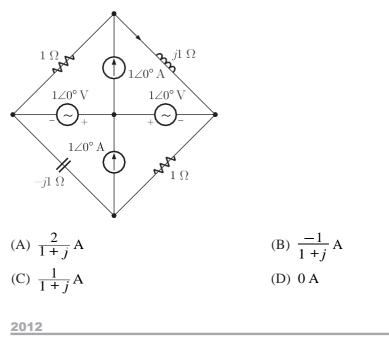
(A) zero

- (B) a step function
- (C) an exponentially decaying function
- (D) an impulse function

The average power delivered to an impedance (4 – *j*3) W 5 cos (100*pt* + 100) A is (A) 44.2 W (B) 50 W (C) 62.5 W (D) 125 W by a current



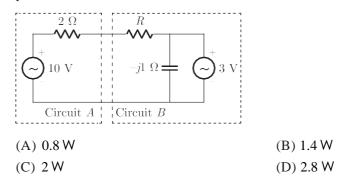
In the circuit shown below, the current through the inductor is



TWO MARKS

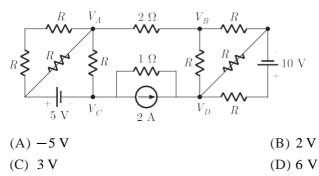


Assuming both the voltage sources are in phase, the value of R for which maximum power is transferred from circuit A to circuit B is



Q. 14

If $V_A - V_B = 6$ V then $V_C - V_D$ is



Common Data For Q. 15 and 16 :

With 10 V dc connected at port A in the linear nonreciprocal two-port network shown below, the following were observed :

- (i) 1 W connected at port B draws a current of 3 A
- (ii) 2.5 W connected at port *B* draws a current of 2 A



Q. 15 With 10 V dc connected at port A, the current drawn by 7 W connected at port *B* is /7 / $(\mathbf{A}) \mathbf{a}$

(A) $3//A$	(B) 5/7 A
(C) 1 A	(D) 9/7 A

Q. 16

For the same network, with 6 V dc connected at port A, 1 W connected at port B draws 7 / 3 A. If 8 V dc is connected to port A, the open circuit voltage at port B is (A) 6 V (B) 7 V

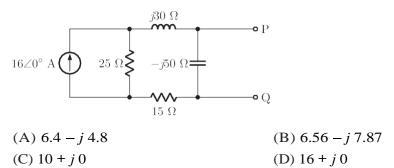


2011

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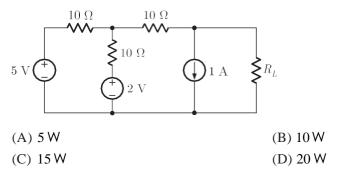
Q. 17

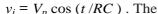
In the circuit shown below, the Norton equivalent current in amperes with respect to the terminals P and Q is



Q. 18

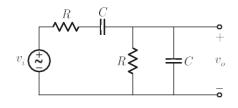
In the circuit shown below, the value of R_L such that the power transferred to R_L is maximum is



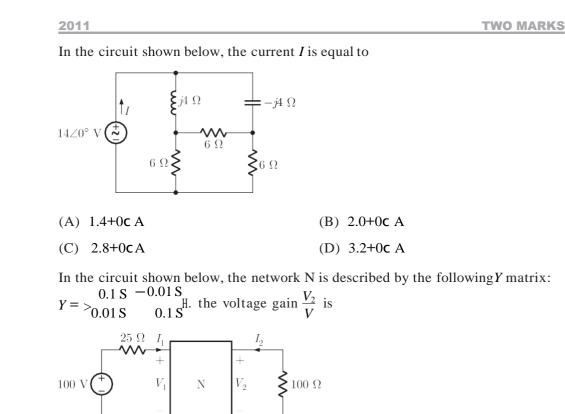




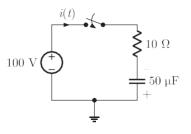
The circuit shown below is driven by a sinusoidal input $v_i = V_p \cos(t/RC)$. The steady state output v_o is



(A) $(V_p/3)\cos(t/RC)$	(B) $(V_p/3)\sin(t/RC)$
(C) $(V_p/2)\cos(t/RC)$	(D) $(V_p/2) \sin(t/RC)$



In the circuit shown below, the initial charge on the capacitor is 2.5 mC, with the voltage polarity as indicated. The switch is closed at time t = 0. The current i(t) at a time t after the switch is closed is



- (A) $i(t) = 15 \exp(-2 \# 10^3 t) \text{ A}$ (B) $i(t) = 5 \exp(-2 \# 10^3 t) \text{ A}$
- (C) $i(t) = 10 \exp(-2 \# 10^3 t) \text{ A}$
- (D) $i(t) = -5 \exp(-2 \# 10^3 t) \text{ A}$

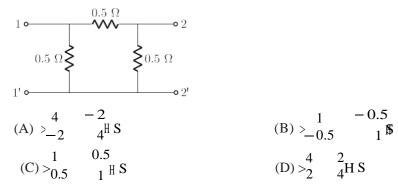
Q. 20

Q. 21

TWO MARKS

Q. 23

For the two-port network shown below, the short-circuit admittance parameter matrix is



Q. 24

For parallel *RLC* circuit, which one of the following statements is NOT correct ?

- (A) The bandwidth of the circuit decreases if R is increased
- (B) The bandwidth of the circuit remains same if L is increased
- (C) At resonance, input impedance is a real quantity
- (D) At resonance, the magnitude of input impedance attains its minimum value.

<u>2010</u>

Q. 25

In the circuit shown, the switch S is open for a long time and is closed at t=0. The current i(t) for $t \$ 0^+$ is

1.5 A
1.5 A
1.5 A
10
$$\Omega$$

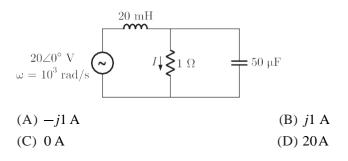
 $i(t)\downarrow \leq 10 \Omega$
 $i(t)\downarrow \leq 10 \Omega$
 $i(t)\downarrow \leq 10 \Omega$
 $i(t)\downarrow \leq 10 \Omega$

(A)
$$i(t) = 0.5 - 0.125e^{-1000t} A$$

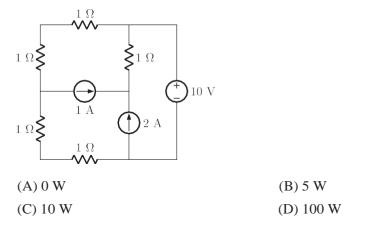
(B) $i(t) = 1.5 - 0.125e^{-1000t} A$
(C) $i(t) = 0.5 - 0.5e^{-1000t} A$
(D) $i(t) = 0.375e^{-1000t} A$

Q. 26

The current *I* in the circuit shown is



In the circuit shown, the power supplied by the voltage source is

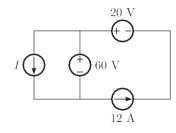


GATE 2009

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Q. 28

In the interconnection of ideal sources shown in the figure, it is known that the 60 V source is absorbing power.

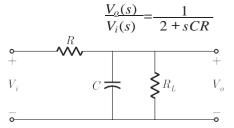


Which of the following can be the value of the current source *I* ? (A) 10 A (B) 13 A

(C) 15 A	(D) 18 A

Q. 29

If the transfer function of the following network is



The value of the load resistance R_L is (A) $\frac{R}{4}$ (C) R

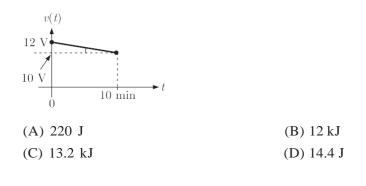
Q. 30

A fully charged mobile phone with a 12 V battery is good for a 10 minute talktime. Assume that, during the talk-time the battery delivers a constant current of 2 A and its voltage drops linearly from 12 V to 10 V as shown in the figure. How much energy does the battery deliver during this talk-time?

R

2

(D) 2*R*

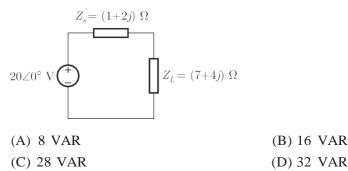


GATE 2009

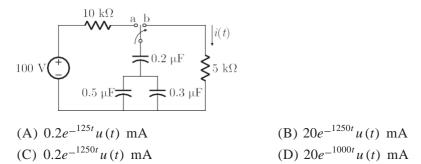
TWO MARK

Q. 31

An AC source of RMS voltage 20 V with internal impedance $Z_s = (1 + 2j)$ W feeds a load of impedance $Z_L = (7 + 4j)$ W in the figure below. The reactive power consumed by the load is

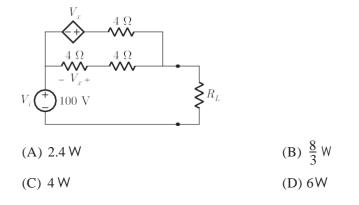


The switch in the circuit shown was on position a for a long time, and is move to position b at time t = 0. The current i(t) for t > 0 is given by



Q. 33

In the circuit shown, what value of R_L maximizes the power delivered to R_L ?



The time domain behavior of an RL circuit is represented by

 $L\frac{di}{dt} + Ri = V_0(1 + Be^{-Rt/L}\sin t)u(t).$

For an initial current of $i(0) = \frac{V_0}{R}$, the steady state value of the current is given

by
(A)
$$i(t) "\frac{V_0}{R}$$
(B) $i(t) "\frac{2V_0}{R}$
(C) $i(t) "\frac{V_0}{R}(1+B)$
(D) $i(t) "\frac{2V_0}{R}(1+B)$

GATE 2008

GATE 2008

ONE MARK

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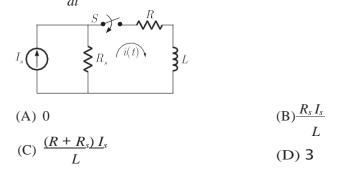
Q. 35

In the following graph, the number of trees (P) and the number of cut-set (Q) are



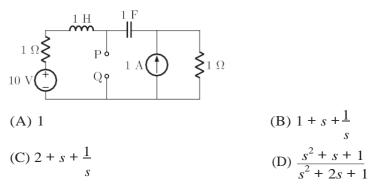
Q. 36

In the following circuit, the switch S is closed at t = 0. The rate of change of current $\frac{di}{dt}(0^+)$ is given by

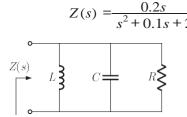


Q. 37

The Thevenin equivalent impedance Z_{th} between the nodes P and Q in the following circuit is



The driving point impedance of the following network is given by

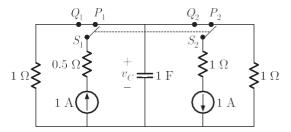


The component values are (A) L = 5 H, R = 0.5 W, C = 0.1 F (B) L = 0.1 H, R = 0.5 W, C = 5 F (C) L = 5 H, R = 2 W, C = 0.1 F (D) L = 0.1 H, R = 2 W, C = 5 F

Q. 39

The circuit shown in the figure is used to charge the capacitor C alternately from two current sources as indicated. The switches S_1 and S_2 are mechanically coupled and connected as follows:

For 2n T # t # (2n + 1)T, $(n = 0, 1, 2,..) S_1$ to P_1 and S_2 to P_2 For (2n + 1) T # t # (2n + 2) T, $(n = 0, 1, 2,...) S_1$ to Q_1 and S_2 to Q_2



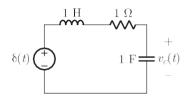
Assume that the capacitor has zero initial charge. Given that u(t) is a unit step function, the voltage $v_c(t)$ across the capacitor is given by

(A)
$$\int_{n=1}^{7} (-1)^n tu(t-nT)$$

(B) $u(t) + 2 \int_{n=1}^{3} (-1)^n u(t-nT)$
(C) $tu(t) + 2 \int_{n=1}^{3} (-1)^n u(t-nT)(t-nT)$
(D) $\int_{n=1}^{3} 60.5 - e^{-(t-2nT)} + 0.5e^{-(t-2nT)} - T$

Common Data For Q.40 and 41 :

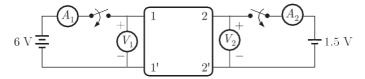
The following series *RLC* circuit with zero conditions is excited by a unit impulse functions d(t).



For t > 0, the output voltage $v_C \wedge t$ h is (B) (A) $\Lambda e^{2} - e^{2} h$ *te* 2*t* (C) $\frac{7}{3}$ $\frac{3}{10}$ tm $\frac{2}{3}$ $\frac{-3}{10}$ cosc $\frac{\sqrt{3}}{2}$ $\frac{\sqrt{2}}{3} \operatorname{sinc}_{2} t \operatorname{m}_{2}$ $\frac{2}{\sqrt{3}} e^{\frac{-1}{2}t} \quad \sqrt{3}$ (D) For $t \neq \frac{2}{39}e^{\frac{-1}{2}t}$ voltage across the resistor is Q. 41 (A) ____ $e^{\sqrt{2}}$ 3 $(B) \underbrace{\frac{1}{2} e^{-\frac{1}{2}t} = \cos c}_{2} e^{-\frac{1}{2}t} \sin c}_{2} \frac{1}{\sqrt{3}t} = \frac{1}{\sqrt{3}} \sin c \frac{\sqrt{3}t}{\sqrt{3}t} = \cos c \frac{\sqrt{3}t}{\sqrt{3}t}$ $\frac{\overline{\sqrt{3}}}{\frac{2}{\sqrt{3}}} e_{\frac{-1}{2}t} \csc \frac{\sqrt{3}}{2} t \text{ m}$ (D)

Statement for linked Answers Questions 42 and 43:

A two-port network shown below is excited by external DC source. The voltage and the current are measured with voltmeters V_1 , V_2 and ammeters. A_1, A_2 (all assumed to be ideal), as indicated



Under following conditions, the readings obtained are: (1) S_1 -open, S_2 - closed $A_1 = 0$, $V_1 = 4.5$ V, $V_2 = 1.5$ V, $A_2 = 1$ A (2) S_1 -open, S_2 - closed $A_1 = 4$ A, $V_1 = 6$ V, $V_2 = 6$ V, $A_2 = 0$

Q. 42

Q. 43

Q. 44

The <i>z</i> -parameter matrix for 1.5 1.5	this network is 1.5 4.5
$(A) = 4.5 \ 1.5^{G}$	(B) = $1.5 4.5^{\text{G}}$
(C) = $1.5 \ 1.5^{\text{G}}$	(D) = $1.5 \ 4.5^{\circ}$
The <i>h</i> -parameter matrix for -3 3	this network is $-3 -1$
$(A) = \frac{1}{3} 1 \frac{0.67}{3}$	(B) = $\frac{3}{3} 0.67^{G}$
$(C) =_{1 0.67} G$	$^{(D)} = -3 - 0.67^{G}$
GATE 2007	ONE M

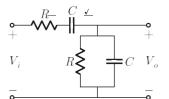
An independent voltage source in series with an impedance $Z_s = R_s + jX_s$ delivers a maximum average power to a load impedance Z_L when (\mathbf{D}) **7**

(A)
$$Z_L = R_s + jX_s$$

(B) $Z_L = R_s$
(C) $Z_L = jX_s$
(D) $Z_L = R_s - jX_s$

Q. 40

The *RC* circuit shown in the figure is



(A) a low-pass filter(C) a band-pass filter

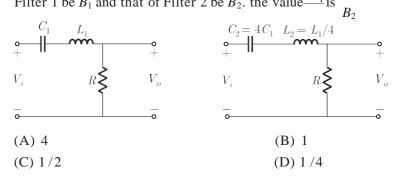


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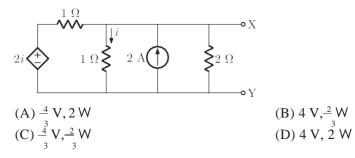
Q. 46

Two series resonant filters are as shown in the figure. Let the 3-dB bandwidth of Filter 1 be B_1 and that of Filter 2 be B_2 . the value B_1 is



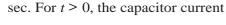


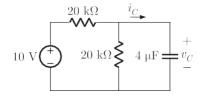
For the circuit shown in the figure, the Thevenin voltage and resistance looking into X - Y are



Q. 48

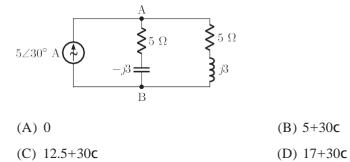
In the circuit shown, v_c is 0 volts at t = 0 sec. For $i_c(t)$, where t is in seconds is given by





- (A) $0.50 \exp(-25t)$ mA
- (B) $0.25 \exp(-25t)$ mA
- (C) $0.50 \exp(-12.5t)$ mA
- (D) $0.25 \exp(-6.25t)$ mA

In the ac network shown in the figure, the phasor voltage V_{AB} (in Volts) is



GATE 2006

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Q. 50

A two-port network is represented by *ABCD* parameters given by $V_1 \qquad A \quad B \quad V_2$

$$=_{I_1}^{V_1} \mathbb{G} = =_C^{A} D \mathbb{G} = -_{I_2}^{V_2} \mathbb{G}$$

If port-2 is terminated by R_L , the input impedance seen at port-1 is given by (A) $\frac{A + BR_L}{C + DR_L}$ (B) $\frac{AR_L + C}{BR_L + D}$

(C)
$$\frac{DR_L + A}{BR_L + C}$$
 (D) $\frac{B + AR_L}{D + CR_L}$

Q. 51

In the two port network shown in the figure below, Z_{12} and Z_{21} and respectively

(A) r_e and br_0	(B) 0 and $-br_0$
. ,	
(C) 0 and br_o	(D) r_e and $-br_0$

The first and the last critical frequencies (singularities) of a driving point impedance function of a passive network having two kinds of elements, are a pole and a zero respectively. The above property will be satisfied by

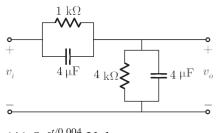
(A) *RL* network only(B) *RC* network only(C) *LC* network only(D) *RC* as well as *RL* networks

Q. 53

Q. 52

A 2 mH inductor with some initial current can be represented as shown below, where s is the Laplace Transform variable. The value of initial current is

0.002s 1 mV (A) 0.5 A (C) 1.0 A (B) 2.0 A (D) 0.0 A **Q. 54** In the figure shown below, assume that all the capacitors are initially uncharged. If $v_i(t) = 10u(t)$ Volts, $v_o(t)$ is given by

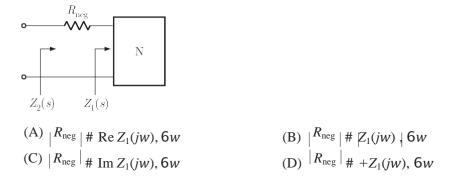


(A) 8e^{-t/0.004} Volts
(C) 8u (t) Volts

(B) $8(1 - e^{-t/0.004})$ Volts (D) 8 Volts

Q. 55

A negative resistance R_{neg} is connected to a passive network N having driving point impedance as shown below. For $Z_2(s)$ to be positive real,

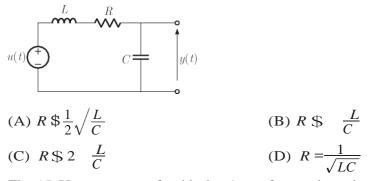


GATE 2005

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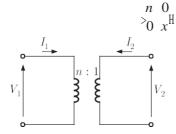
Q. 56

The condition on R, L and C such that the step response y(t) in the figure has no oscillations, is



Q. 57

The ABCD parameters of an ideal n: 1 transformer shown in the figure are



The value of *x* will be

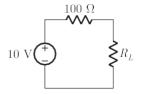
(A)
$$n$$
 (B) $\frac{1}{n}$

(C) n^2 (D) $\frac{1}{n^2}$

Q. 58 In a series *RLC* circuit, R = 2 kW, L = 1 H, and $C = \frac{1}{400} \text{ The resonant}$ frequency is (A) $2 \# 10^4 \text{ Hz}$ (B) $\frac{1}{p} \# 10^4 \text{ Hz}$ (C) 10^4 Hz (D) $2p \# 10^4 \text{ Hz}$

Q. 59

The maximum power that can be transferred to the load resistor R_L from the voltage source in the figure is



(A) 1 W	(B) 10 W
(C) 0.25 W	(D) 0.5 W

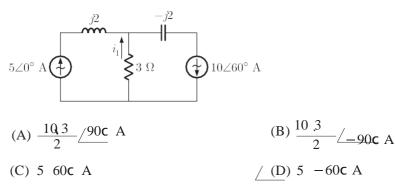
Q. 60 The first and the last critical frequency of an *RC* -driving point impedance function must respectively be
 (A) a zero and a pole
 (B) a zero and a zero
 (C) a pole and a pole
 (D) a pole and a zero

GATE 2005

TWO MARKS

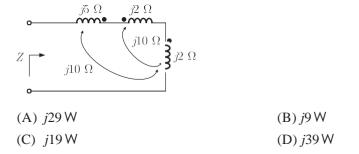
Q. 61

For the circuit shown in the figure, the instantaneous current $i_1(t)$ is

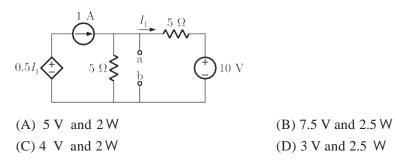


Q. 62

Impedance Z as shown in the given figure is

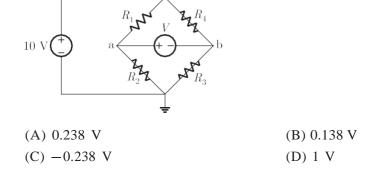


Q. 63 For the circuit shown in the figure, Thevenin's voltage and Thevenin's equivalent resistance at terminals a - b is



Q. 64

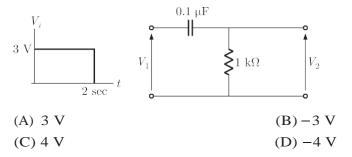
If $R_1 = R_2 = R_4 = R_1$ and $R_3 = 1.1R_1$ in the bridge circuit shown in the figure, then the reading in the ideal voltmeter connected between a and b is





The *h* parameters of the circuit shown in the figure are

A square pulse of 3 volts amplitude is applied to C - R circuit shown in the figure. The capacitor is initially uncharged. The output voltage V_2 at time t = 2 sec is

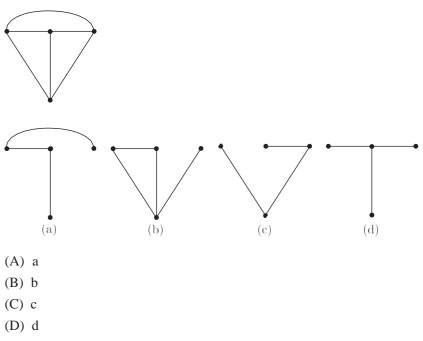


GATE 2004

ONE MARK

Q. 67

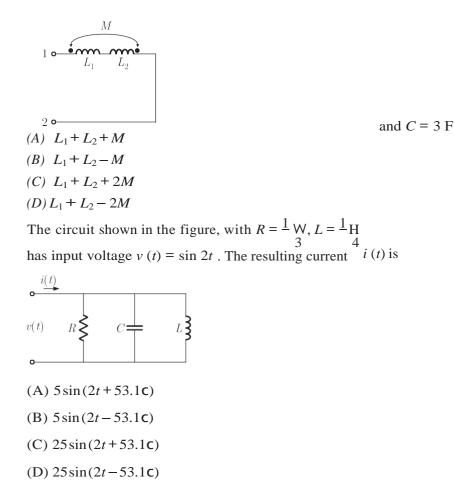
Consider the network graph shown in the figure. Which one of the following is NOT a 'tree' of this graph ?



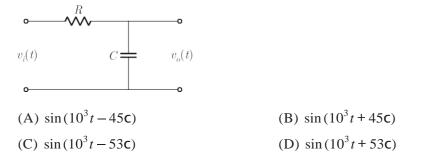
Q. 68

Q. 69

The equivalent inductance measured between the terminals 1 and 2 for the circuit shown in the figure is

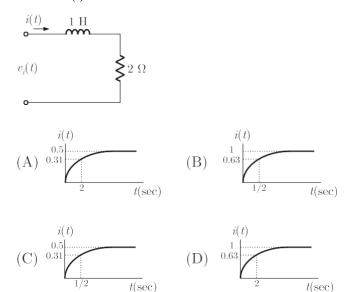


Q. 70 For the circuit shown in the figure, the time constant RC = 1 ms. The input voltage is $v_i(t) = \sqrt{2} \sin 10^3 t$. The output voltage $v_o(t)$ is equal to



Q.71

For the R - L circuit shown in the figure, the input voltage $v_i(t) = u(t)$. The current *i*(*t*) is



GATE 2004

TWO MARKS

Q. 72

For the lattice shown in the figure, $Z_a = j2$ W and $Z_b = 2$ W. The values of the open circuit impedance parameters $6z@==_{Z_{21}}^{Z_{11}} Care$

The circuit shown in the figure has initial current $i_L(0^-) = 1$ A through the inductor and an initial voltage $v_C(0^-) = -1$ V across the capacitor. For input v(t) = u(t), the Laplace transform of the current i(t) for t \$ 0 is

$$i(t) \to 1 \Omega \to 1 H$$

$$v(t) \to 1F$$

$$(A) \frac{s}{s^2 + s + 1}$$

$$(C) \frac{s - 2}{s^2 + s + 1}$$

$$(B) \frac{s + 2}{s^2 + s + 1}$$

$$(D) \frac{1}{s^2 + s + 1}$$

Q. 74 The transfer function $H(s) = \frac{V_o(s)}{V_i(s)}$ of an *RLC* circuit is given by

$$H(s) = \frac{10^6}{s^2 + 20s + 10^6}$$

The Quality factor (Q-factor) of this circuit is(A) 25(B) 50(C) 100(D) 5000

Q. 75

Q. 73

For the circuit shown in the figure, the initial conditions are zero. Its transfer function $H(s) = \frac{V_c(s)}{s}$ is

$$V_{i}(s) = V_{i}(s)$$

$$V_{i}(t) = 10 \text{ mH}$$

$$v_{o}(t) = v_{o}(t)$$

$$(A) = \frac{1}{s^{2} + 10^{6}s + 10^{6}}$$

$$(B) = \frac{10^{6}}{s^{2} + 10^{3}s + 10^{6}}$$

$$(C) = \frac{10^{3}}{s^{2} + 10^{3}s + 10^{6}}$$

$$(D) = \frac{10^{6}}{s^{2} + 10^{6}s + 10^{6}}$$

Q. 76

Consider the following statements S1 and S2

S1 : At the resonant frequency the impedance of a series RLC circuit is zero. S2 : In a parallel GLC circuit, increasing the conductance G results in increase in its Q factor.

Which one of the following is correct?

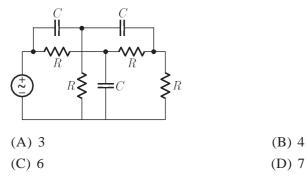
- (A) S1 is FALSE and S2 is TRUE
- (B) Both S1 and S2 are TRUE
- (C) S1 is TRUE and S2 is FALSE
- (D) Both S1 and S2 are FALSE

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ONE MARK

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Q. 77
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The minimum number of equations required to analyze the circuit shown in the figure is



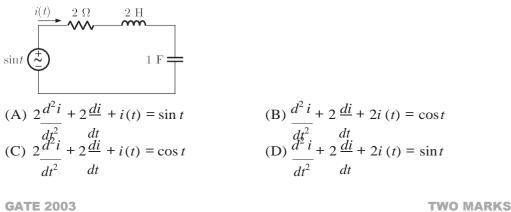


A source of angular frequency 1 rad / sec has a source impedance consisting of 1 W resistance in series with 1 H inductance. The load that will obtain the maximum power transfer is

- (A) 1 W resistance
- (B) 1 W resistance in parallel with 1 H inductance
- (C) 1 W resistance in series with 1 F capacitor
- (D) 1 W resistance in parallel with 1 F capacitor
- A series RLC circuit has a resonance frequency of 1 kHz and a quality factor Q. 79 Q = 100. If each of R, L and C is doubled from its original value, the new Q of the circuit is
 - (A) 25
 - (B) 50
 - (C) 100
 - (D) 200

```
Q. 80
```

The differential equation for the current i(t) in the circuit of the figure is



GATE 2003

Q. 81

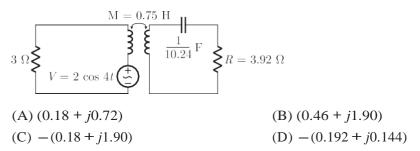
Twelve 1 W resistance are used as edges to form a cube. The resistance between two diagonally opposite corners of the cube is

(A) $\frac{5}{6}$ W (B) 1 W (C) $\frac{6}{5}$ W (D) $\frac{3}{2}$ W

 $i_1(t)$ and $i_2(t)$ respectively. The

Q. 82

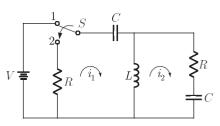
The current flowing through the resistance R in the circuit in the figure has the form $P \cos 4t$ where P is



Common Data For Q. 83 and 84 :

Assume that the switch S is in position 1 for a long time and thrown to position 2 at t = 0.

equations for the loop currents $I_1(s)$ and $I_2(s)$ for the circuit shown in the figure, after



At $t = 0^+$, the current i_1 is (A) $\frac{-V}{2R}$ (B) $\frac{-V}{R}$ (C) $\frac{-V}{4R}$ (D) zero

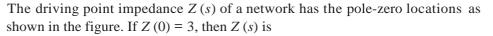
 $I_1(s)$ and $I_2(s)$ are the Laplace transforms of

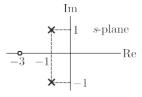
Q. 84

Q. 83

the switch is brought from position 1 to position 2 at
$$t = 0$$
, are
(A) $> \frac{R + Ls + \frac{1}{Cs}}{-Ls} = \frac{-Ls}{R + \frac{1}{Cs}} = \frac{I_1(s)}{I_2(s)} \mathbf{G} = \frac{\frac{V}{s}}{0} \mathbf{G}$
(B) $> \frac{R + Ls + \frac{1}{Cs}}{-Ls} = \frac{-Ls}{R + \frac{1}{Cs}} = \frac{I_1(s)}{I_2(s)} \mathbf{G} = \frac{-\frac{1}{5}}{0} \mathbf{G}$
(C) $> \frac{R + Ls + \frac{1}{Cs}}{-Ls} = \frac{-Ls}{R + Ls + \frac{1}{Cs}} = \frac{I_1(s)}{I_2(s)} \mathbf{G} = \frac{-\frac{1}{5}}{0} \mathbf{G}$
(D) $> \frac{R + Ls + \frac{1}{Cs}}{-Ls} = \frac{-Cs}{R + Ls + \frac{1}{Cs}} = \frac{I_1(s)}{I_2(s)} \mathbf{G} = \frac{V}{s} \mathbf{G}$

Q. 85





(A)
$$\frac{3(s+3)}{s^2+2s+3}$$

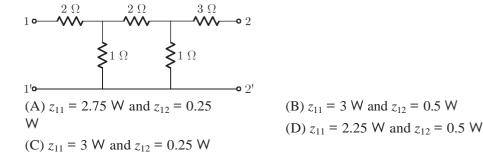
(B) $\frac{2(s+3)}{s^2+2s+2}$
(B) $\frac{2(s+3)}{s^2+2s+2}$
(D) $\frac{2(s-3)}{s^2-2s-3}$

An input voltage v(t) = 10 $2\cos(t+10c) + 10$ $5\cos(2t+10c)$ V is applied to a series combination of resistance R = 1 W and an inductance L = 1 H. The resulting steady-state current i(t) in ampere is

- (A) $10 \cos(t + 55\mathbf{c}) + 10 \cos(2t + 10\mathbf{c} + \tan^{-1}2)$
- (B) $10\cos(t+55c)+10\sqrt{\frac{3}{2}}\cos(2t+55c)$
- (C) $10\cos(t 35\mathbf{c}) + 10\cos(2t + 10\mathbf{c} \tan^{-1}2)$
- (D) $10\cos(t-35c) + \sqrt{\frac{3}{2}}\cos(2t-35c)$

Q. 87

The impedance parameters z_{11} and z_{12} of the two-port network in the figure are

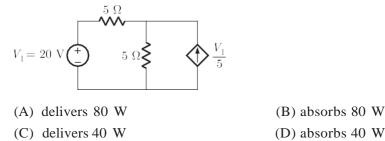


GATE 2002

Q. 88

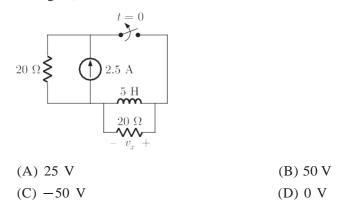
ONE MARK

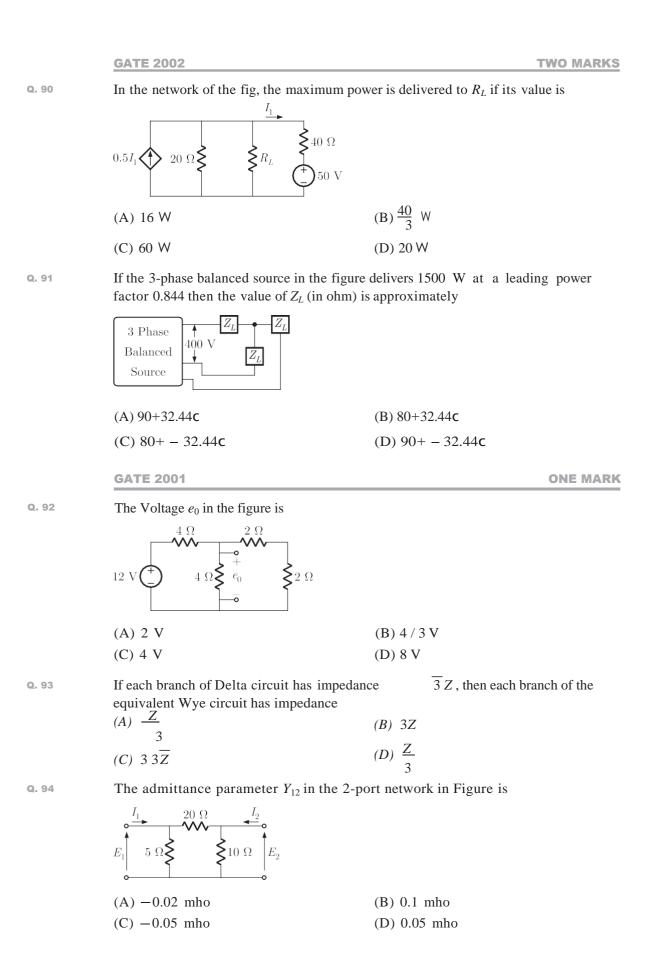
The dependent current source shown in the figure



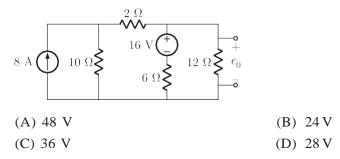
Q. 89

In the figure, the switch was closed for a long time before opening at t = 0. The voltage v_x at $t = 0^+$ is



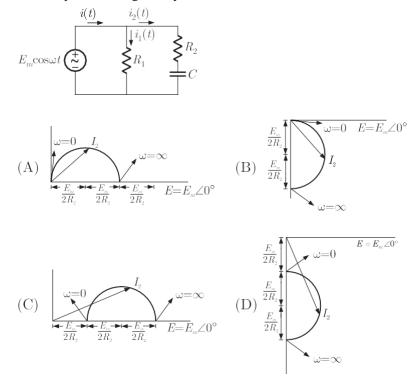


The voltage e_0 in the figure is

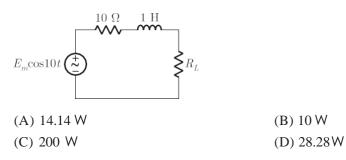


Q. 96

When the angular frequency w in the figure is varied 0 to 3, the locus of the current phasor I_2 is given by



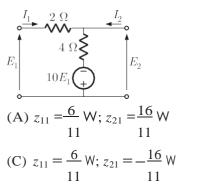
Q. 97 In the figure, the value of the load resistor R_L which maximizes the power delivered to it is

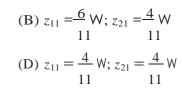


TWO MARKS

Q. 98

The **Sparameters** z_{11} and z_{21} for the 2-port network in the figure are



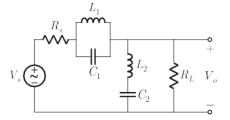


GATE 2000



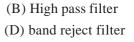


The circuit of the figure represents a



(A) Low pass filter

(C) band pass filter



Q. 100

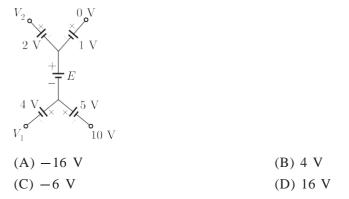
 $\begin{array}{c} 1 \Omega \\ 1 \Pi \\ 1 \Pi$

In the circuit of the figure, the voltage v(t) is

(A)
$$e^{at} - e^{bt}$$

(B) $e^{at} + e^{bt}$
(C) $ae^{at} - be^{bt}$
(D) $ae^{at} + be^{bt}$

In the circuit of the figure, the value of the voltage source*E* is

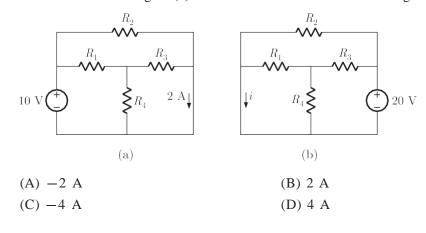


GATE 2000



Q. 102

Use the data of the figure (a). The current *i* in the circuit of the figure (b)

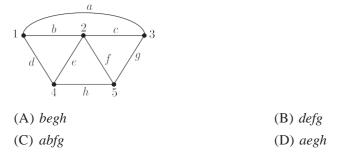


GATE 1999

ONE MARK

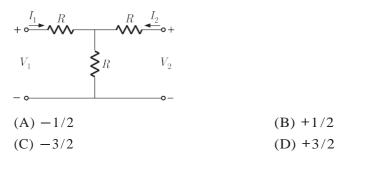
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Q. 103
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Identify which of the following is NOT a tree of the graph shown in the given figure is





A 2-port network is shown in the given figure. The parameter h_{21} for this network can be given by

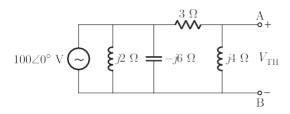


GATE 1999

TWO MARK

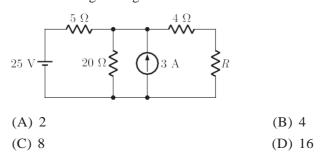
Q. 105

The Thevenin equivalent voltage V_{TH} appearing between the terminals A and B of the network shown in the given figure is given by



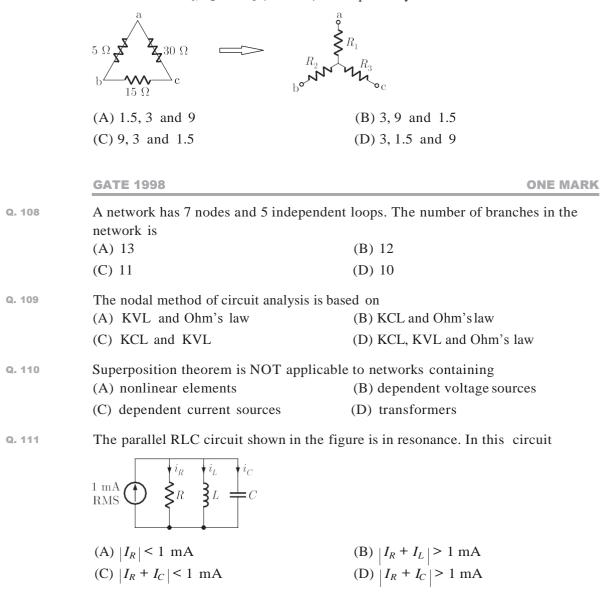
(A)
$$j16(3-j4)$$
(B) $j16(3+j4)$ (C) $16(3+j4)$ (D) $16(3-j4)$

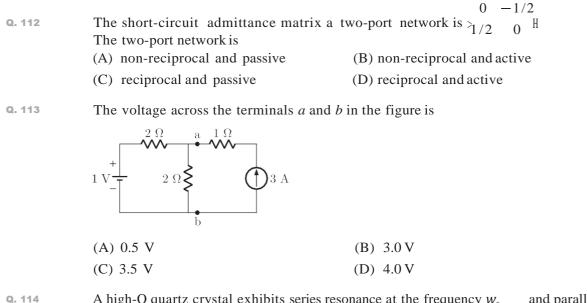
The value of R (in ohms) required for maximum power transfer in the network shown in the given figure is



Q. 107

A Delta-connected network with its Wye-equivalent is shown in the given figure. The resistance R_1 , R_2 and R_3 (in ohms) are respectively





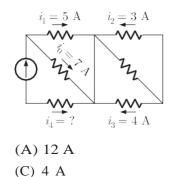
- A high-Q quartz crystal exhibits series resonance at the frequency w_s and parallel resonance at the frequency w_p . Then
 - (A) w_s is very close to, but less than w_p
 - (B) $W_s \leq W_p$
 - (C) w_s is very close to, but greater than w_p
 - (D) $W_s >> W_p$

GATE 1997

ONE MARK

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Q. 115
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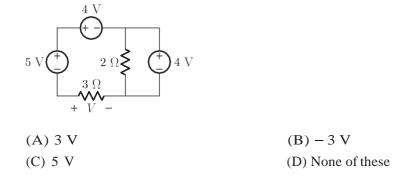
The current i_4 in the circuit of the figure is equal to

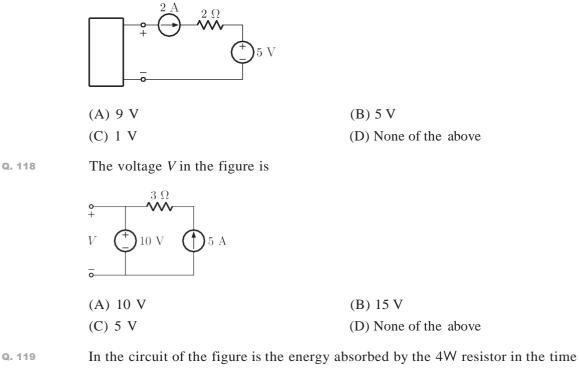


(B) -12 A(D) None or these

0	-1	4	G
496 =			0

The voltage V in the figure equal to

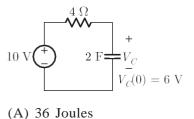




The voltage V in the figure is always equal to

Q. 117

interval (0, 3) is

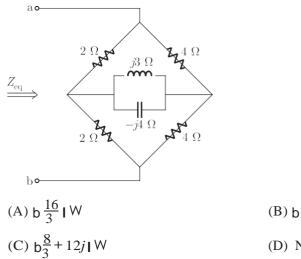


(C) 256 Joules

(B) 16 Joules (D) None of the above

Q. 120

In the circuit of the figure the equivalent impedance seen across terminals a, b, is



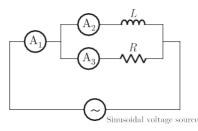
(B) $b\frac{8}{3}$ W (D) None of the above

GΔT	E.	4	a	a	6	
GAI			J	J	υ.	

ONE MARK

Q. 121

In the given figure, A_1 , A_2 and A_3 are ideal ammeters. If A_2 and A_3 read 3 A and 4 A respectively, then A_1 should read



(A) 1 A
(B) 5 A
(C) 7 A
(D) None of these

Q. 122

The number of independent loops for a network with n nodes and b branches is

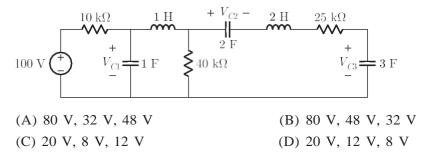
- (A) n 1
- $(B) \ b-n$
- (C) b n + 1
- (D) independent of the number of nodes

GATE 1996

TWO MARKS

Q. 123

The voltages V_{C1} , V_{C2} , and V_{C3} across the capacitors in the circuit in the given figure, under steady state, are respectively.



SOLUTIONS

Sol. 1

Option (B) is correct. In the equivalent star connection, the resistance can be given as

$$R_{C} = \frac{R_{b} R_{a}}{R_{a} + R_{b} + R_{c}}$$
$$R_{B} = \frac{R_{a} R_{c}}{R_{a} + R_{b} + R_{c}}$$
$$R_{A} = \frac{R_{b} R_{c}}{R_{a} + R_{b} + R_{c}}$$

So, if the delta connection components R_a , R_b and R_c are scaled by a factor k then

$$R_{A} = \frac{\frac{kR_{b}hkR_{c}h}{kR_{a}+kR_{b}+kR_{c}}}{kR_{a}+kR_{b}+kR_{c}} = \frac{k^{2}}{k}\frac{R_{b}R_{c}}{R_{a}+R_{b}+R_{c}} = kR_{A}$$

Hence, it is also scaled by a factor k

Sol. 2 Option (D) is correct.

For the given capacitance, C = 100mF in the circuit, we have the reactance.

$$X_C = \frac{1}{sc} = \frac{1}{s \# 100 \# 10^{-6}} = \frac{10}{s}$$

So,

$$\frac{V \wedge sh}{V_1 \wedge sh} = \frac{\frac{10^4}{s} + 10^4}{\frac{10^4}{s} + 10^4 + \frac{10^4}{s}} = \frac{s+1}{s+2}$$

Sol. 3 Option (C) is correct.

For the purely resistive load, maximum average power is transferred when $R_L = \sqrt{R} \frac{2}{Th} + X \frac{2}{Th}$

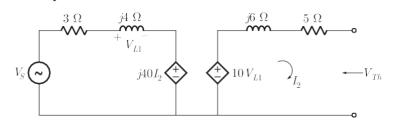
where $R_{Th} + jX_{Th}$ is the equivalent the venin (input) impedance of the circuit. Hence, we obtain

$$R_L = \sqrt{4^2 + 3^2} 5 \text{ W}$$

Sol. 4

Option (C) is correct.

For evaluating the equivalent the venin voltage seen by the load R_L , we open the circuit across it (also if it consist dependent source). The equivalent circuit is shown below



As the circuit open across R_L so

$$I_2 = 0$$

or, $j40I_2 = 0$

i.e., the dependent source in loop 1 is short circuited. Therefore,

$$V_{L1} \stackrel{\wedge}{=} \frac{b^{2}4}{j\overline{4}} \frac{v_{s}}{7}$$

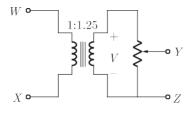
$$V_{Th} = 10 \ V_{L1} = \frac{j40}{j4} \frac{100}{5} \frac{53.13c}{553.13c} = \frac{40.90c}{553.13c} \frac{100}{553.13c} \frac{53.13c}{553.13c}$$

$$= 800.90c$$

Sol. 5

Option (C) is correct. For the given transformer, we have

$$\frac{V}{V_{WX}} = \frac{1.25}{1}$$



Since,

and

 $\frac{V_{YZ}}{V} = 0.8$ (attenuation factor)

So,	$\frac{V_{YZ}}{V_{WX}}$ = ^0.8h^1.25h = 1
-----	---

or,
at

$$V_{YZ} = V_{WX}$$

 $V_{WX} = 100 \text{ V}; \quad \frac{V_{YZ_1}}{V_{WX_2}} = \frac{100}{100}$
at
 $V_{WZ_2} = 100 \text{ V}; \quad \frac{V_{WX_1}}{V_{WX_2}} = \frac{100}{100}$

Sol. 6

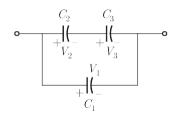
The quality factor of the inductances are given by

$$q_1 = \frac{wL_1}{R_1}$$
$$q_2 = \frac{wL_2}{R_2}$$

So, in series circuit, the effective quality factor is given by

$$Q = \frac{\left| \frac{X_{Leq}}{R_{eq}} \right|}{R_{eq}} = \frac{wL_1 + wL_2}{R_1 + R_2}$$
$$= \frac{\frac{wL_1}{R_1 + R_2} + \frac{wL_2}{R_1 + R_2}}{\frac{1}{R_2} + \frac{1}{R_1}} = \frac{\frac{q_1}{R_2} + \frac{q_2}{R_2}}{\frac{1}{R_2} + \frac{1}{R_1}} = \frac{\frac{q_1R + q_2R}{R_1 + R_2}}{R_1 + R_2}$$
Option (C) is correct.

Sol. 7



Option (C) is correct.

Consider that the voltage across the three capacitors C_1 , C_2 and C_3 are V_1 , V_2 and V_3 respectively. So, we can write

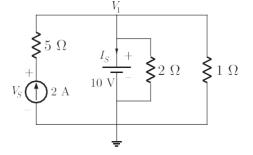
$$\frac{V_2}{V_3} = \frac{C_3}{C_2} \qquad \dots (1)$$

Since, Voltage is inversely proportional to capacitance

Now, given that $C_1 = 10 \ mF$; $^V_1h_{max} = 10V$ $C_2 = 5 mF$; $^V_2h_{max} = 5 V$ $C_3 = 2 mF$; $^V_3h_{max} = 2V$ So, from Eq (1) we have $\frac{V_2}{V_2} = \frac{2}{5}$ $V_3h_{max} = 2$ for $V_2 = \frac{2 \# 2}{5} = 0.8$ volt < 5 We obtain, $V_2 < V_2 h_{\text{max}}$ i.e., Hence, this is the voltage at $\overline{C_2}$. Therefore, $V_3 = 2$ volt $V_2 = 0.8$ volt $V_1 = V_2 + V_3 = 2.8$ volt and Now, equivalent capacitance across the terminal is $C_{eq} = \frac{C_2 C_3}{C_2 C_3} + C_1 = \frac{5 \# 2}{2} + 10 = \frac{80}{2} \text{ mF}$ $C_2 + C_3$ 5 + 27 Equivalent voltage is (max. value) $V_{\text{max}} = V_1 = 2.8$ So, charge stored in the effective capacitance is 00

$$Q = C_{eq} V_{max} = b \frac{80}{7} I \# ^2.8h = 32 \text{ mC}$$

Option (D) is correct.



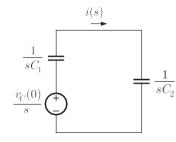
At the node 1, voltage is given as $V_1 = 10$ volt Applying KCL at node 1 $I_S + \frac{V_1}{2} + \frac{V_1}{1} - 2 = 0$ $I_S + \frac{10}{2} + \frac{10}{1} - 2 = 0$ $I_S = -13$ A Also, from the circuit, $V_S - 5 # 2 = V_1$ & $V_S = 10 + V_1 = 20$ volt Sol. 9 Option (C) is correct. Again from the shown circuit, the current in 1 W resistor is $I - \frac{V_1 - 10}{2} - 10$ A

$$I = \frac{V_1}{1} = \frac{10}{1} = 10 \text{ A}$$

Sol. 10 Option (D) is correct.

The *s* -domain equivalent circuit is shown as below.

Sol. 8



$$I(s) = \frac{v_{c}(0) / s}{\frac{1}{C_{1}s} + \frac{1}{C_{2}s}} = \frac{v_{c}(0)}{\frac{1}{C_{1}} + \frac{1}{C_{2}}}$$

$$I(s) = \frac{C_{1}C_{2}}{C_{1} + C_{2}} |(12 \text{ V})| = 12C_{eq} \qquad v_{C}(0) = 12 \text{ V}$$

$$v_{C}(0) = 12 \text{ V}$$

$$v_{C}(0) = 12 \text{ V}$$

Taking inverse Laplace transform for the current in time domain,

$$i(t) = 12C_{eq} d(t)$$
 (Impulse)

$$Z = 4 - j3 = 5 - 36.86$$
CW
 $I = 5/100$ CA

Average power delivered.

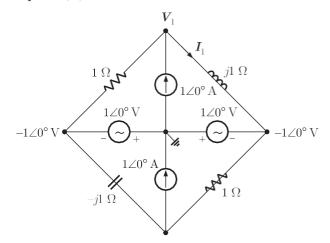
$$P_{avg.} = \frac{1}{2} |\mathbf{I}|^2 Z \cos q = \frac{1}{2} \# 25 \# 5 \cos 36.86 \mathbf{c} = 50 \text{ W}$$

Alternate Method:

$$Z = (4 - j3) \text{ W}, \qquad I = 5 \cos (100pt + 100) \text{ A}$$
$$P_{avg} = \frac{1}{2} \text{Re} |I|^2 Z = \frac{1}{2} \text{ HRe}''(5)^2 \text{ H}(4 - j3), = \frac{1}{2} \text{ H}100 = 50 \text{ W}$$

Sol. 12

Option (C) is correct



Applying nodal analysis at top node.

$$\frac{V_{1} + V_{1} + V_{1} + V_{1} + j_{1}}{j_{1}} = \frac{1}{j_{0}c}$$

$$V_{1}(j_{1}+1) + j_{1} + 1 / 0c = j_{1}$$

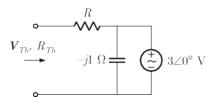
$$V_{1} = \frac{-1}{1+j_{1}}$$

$$\frac{V + j_{0}c}{j_{1}} = \frac{-\frac{1}{j_{1}}j_{1}}{j_{1}} = \frac{j}{(1+j)_{j}} = \frac{1}{1+j}A$$

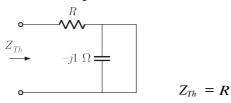
Current

Sol. 13

Option (A) is correct. We obtain The venin equivalent of circuit \boldsymbol{B} .



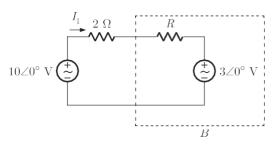
Thevenin Impedance :



Thevenin Voltage :

 $V_{Th} = 3/0$ c V

Now, circuit becomes as



Current in the circuit,
$$I_1 = \frac{10}{2}$$

Power transfer from circuit A to B

$$P = (I_{1}^{2})^{2} R + 3I_{1}$$

$$= \frac{10 - 3}{2 + R} D^{2} R + \frac{3}{2 + R} D$$

$$P = \frac{42 + 70R}{(2 + R)^{2}}$$

$$\frac{dP}{dR} = \frac{(2 + R)^{2} 70 - (42 + 70R) 2(2 + R)}{(2 + R)^{4}} R_{0}^{2} =$$

$$(2 + R) [(2 + R) 70 - (42 + 70R) 2] = 0 \quad \& \quad R = 0.8 \text{ W}$$

+R

or

Sol. 14

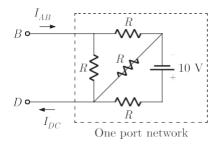
Option (A) is correct.

In the given circuit

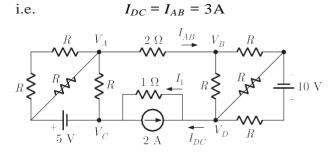
 $V_A - V_B = 6 V$ So current in the branch will be

$$I_{AB} = \frac{6}{2} = 3 \text{ A}$$

We can see, that the circuit is a one port circuit looking from terminal BD as shown below



For a one port network current entering one terminal, equals the current leaving the second terminal. Thus the outgoing current from A to B will be equal to the incoming current from D to C as shown



The total current in the resistor 1 W will be

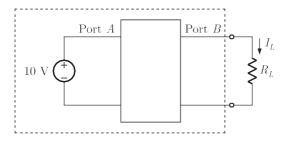
 $I_1 = 2 + I_{DC}$ = 2 + 3 = 5 A $V_{CD} = 1 # (-I_1) = -5 V$ (By writing KCL at node D)

Sol. 15

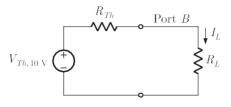
So,

Option (C) is correct.

When 10 V is connected at port A the network is



Now, we obtain Thevenin equivalent for the circuit seen at load terminal, let Thevenin voltage is $V_{Th,10 \text{ V}}$ with 10 V applied at port *A* and Thevenin resistance is R_{Th} .



$$I_L = \frac{V_{Th,10V}}{R_{Th} + R_L}$$

For $R_L = 1$ W, $I_L = 3$ A
$$3 = \frac{V_{Th,10V}}{R_{Th} + 1}$$
...(i)

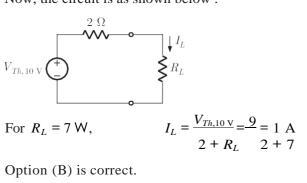
For $R_L = 2.5$ W, $I_L = 2$ A $2 = \frac{V_{Th,10 \,\mathrm{V}}}{R_{Th} + 2.5}$...(ii) Dividing above two $3 - R_{Th} + 2.5$

$$\overline{2} = \overline{R_{Th}} + 1$$
$$3R_{Th} + 3 = 2R_{Th} + 5$$
$$R_{Th} = 2 W$$

Substituting R_{Th} into equation (i)

$$V_{Th,10 \text{ V}} = 3 (2 + 1) = 9 \text{ V}$$

Note that it is a non reciprocal two port network. Thevenin voltage seen at port B depends on the voltage connected at port A. Therefore we took subscript $V_{Th,10 \text{ V}}$. This is Thevenin voltage only when 10 V source is connected at input port A. If the voltage connected to port A is different, then Thevenin voltage will be different. However, Thevenin's resistance remains same. Now, the circuit is as shown below :



тт

Sol. 16 Option (B) is correct.

Now, when 6 V connected at port A let Thevenin voltage seen at port B is $V_{Th,6 \text{ V}}$

. Here
$$R_L = 1$$
 W and $I_L = \frac{7}{3}$ A
 R_{Th}
 $V_{Th, 6 V}$
 $V_{Th, 6 V}$
 $V_{Th, 6 V} = R_{Th} \# \frac{7}{3} + 1 \# \frac{7}{3} = 2 \# \frac{7}{3} + \frac{7}{3} = 7 V$

This is a linear network, so V_{Th} at port *B* can be written as

$$V_{Th} = V_1 a + b$$
where V_1 is the input applied at port A .
We have $V_1 = 10 \text{ V}$, $V_{Th,10 \text{ V}} = 9 \text{ V}$

$$9 = 10a + b$$
...(i)
When $V_1 = 6 \text{ V}$, $V_{Th, 6 \text{ V}} = 9 \text{ V}$

$$7 = 6a + b$$
 ...(ii)

Solving (i) and (ii)

$$a = 0.5, b = 4$$

Thus, with any voltage V_1 applied at port A, Thevenin voltage or open circuit voltage at port B will be

So,

$$V_{Th,V} = 0.5V_1 + 4$$

 $V_1 = 8 \text{ V}$

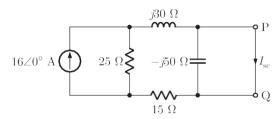
or

$$V_{Th,8V} = 0.5 \# 8 + 4 = 8 = V_{\alpha}$$

(open circuit voltage)

Sol. 17

Replacing P - Q by short circuit as shown below we have



Using current divider rule the current I_{sc} is

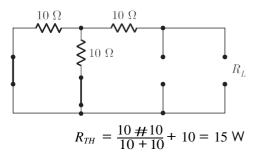
$$I_{sc} = \frac{25}{25 + 15 + j30}(16 \ 0) = (6.4 - j4.8) \text{ A}$$

Sol. 18

Option (C) is correct.

Option (A) is correct.

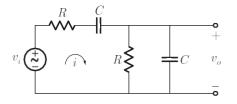
Power transferred to R_L will be maximum when R_L is equal to the Thevenin resistance. We determine Thevenin resistance by killing all source as follows :



Sol. 19

Option (A) is correct.

The given circuit is shown below



For parallel combination of *R* and *C* equivalent impedance is

$$Z_{p} = \frac{R \$ \frac{1}{j WC}}{R + \frac{1}{j WC}} = \frac{R}{1 + j WRC}$$

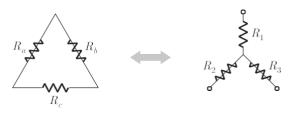
Transfer function can be written as

$$\frac{V_{\text{out}}}{V_{\text{in}}} = \frac{Z_{\text{p}}}{Z_{\text{s}} + Z_{\text{p}}} = \frac{\frac{R}{1 + j \le RC}}{R + \frac{1}{j \le C} + \frac{R}{1 + j \le RC}}$$
$$= \frac{j \le RC}{j \le RC} \frac{j \le RC + (1 + \frac{1}{j \le RC})^2}{(1 + j)^2} \qquad \text{Here } \le \frac{1}{RC}$$

$$\frac{V_{\text{out}}}{V_{\text{in}}} = \frac{j}{(1+j)^2 + j} = \frac{1}{3}$$
$$v_{\text{out}} = b\frac{V_p}{3} \log(t/RC)$$

From star delta conversion we have

Option (B) is correct.



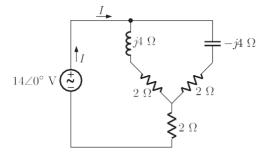
Thus

Here

Thus

$$R_{1} = \frac{R_{a}R_{b}}{R_{a} + R + R_{c}} = \frac{6.6}{6 + 6 + 6} = 2 \text{ W}$$
$$R_{1} = R_{2} = R_{3} = 2 \text{ W}$$

Replacing in circuit we have the circuit shown below :



Now the total impedance of circuit is

$$Z = \frac{(2 + j4)(2 - j4)}{(2 + j4)(2 - j4)} + 2 = 7 W$$
$$I = \frac{14 + 0c}{7} = 2 + 0c$$

Current

or

Sol. 21

Option (D) is correct.

From given admittance matrix we get

$$I_1 = 0.1V_1 - 0.01V_2$$
 and ...(1)

$$I_2 = 0.01V_1 + 0.1V_2 \qquad \dots (2)$$

Now, applying KVL in outer loop;

$$V_2 =- 100I_2 I_2 =- 0.01V_2$$
...(3)

From eq (2) and eq (3) we have

 $-0.01V_2 = 0.01V_1 + 0.1V_2$ $-0.11V_2 = 0.01V_1$ $\frac{V_2}{V_1} = \frac{-1}{11}$

Sol. 22

Option (A) is correct.

Here we take the current flow direction as positive.

At $t = 0^-$ voltage across capacitor is

$$V_C(0^{-}) = -\frac{Q}{C} = -\frac{2.5 \# 10^{-3}}{50 \# 10^{-6}} = -50 \text{ V}$$

Thus

In steady state capacitor behave as open circuit thus

 $V_C(0^+) = -50 \text{ V}$

V(3) = 100 V

Now,

Now

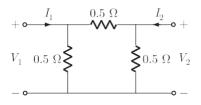
$$V_{C}(t) = V_{C}(\mathbf{3}) + (V_{C}(0^{+}) - V_{C}(\mathbf{3}))e^{-t/RC}$$

= 100 + (-50 - 100) $e^{\frac{-t}{10\#50\#10^{-6}}}$
= 100 - 150 $e^{-(2\#10^{3}t)}$
 $i_{c}(t) = C\frac{dV}{dt}$
= 50 # 10⁻⁶ # 150 # 2 # 10³ $e^{-2\#10^{3}t}$ A
= 15 $e^{-2\#10^{3}t}$
 $i_{c}(t) = 15 \exp(-2 \#10^{3}t)$ A

Sol. 23

Given circuit is as shown below

Option (A) is correct.



Writing node equation at input port

$$I_1 = \frac{V_1}{0.5} + \frac{V_1 - V_2}{0.5} = 4V_1 - 2V_2 \qquad \dots (1)$$

Writing node equation at output port

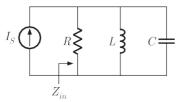
$$I_2 = \frac{V_2}{0.5} + \frac{V_2 - V_1}{0.5} = -2V_1 + 4V_2 \qquad \dots (2)$$

From (1) and (2), we have admittance matrix

$$Y = \frac{4}{2} - \frac{-2}{4^{H}}$$

Sol. 24

Option (D) is correct. A parallel *RLC* circuit is shown below :



Input impedance

At resonance

So,

$$Z_{in} = \frac{1}{\underbrace{1}_{WL} + \frac{1}{jWL} + jWC}$$
$$\frac{1}{WL} = WC$$
$$Z_{in} = \underbrace{1}_{K} = R$$
$$\underbrace{1/R}$$

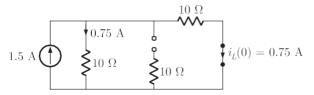
(maximum at resonance)

Thus (D) is not true.

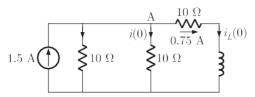
Furthermore bandwidth is w_B i.e $w_B \times \frac{1}{R}$ and is independent of *L*, Hence statements A, B, C, are true.

Sol. 25 Option (A) is correct.

Let the current
$$i(t) = A + Be^{-t/T}$$
 T '' Time constant
When the switch *S* is open for a long time before *t* < 0, the circuit is



At t = 0, inductor current does not change simultaneously, So the circuit is



Current is resistor (AB)

$$i(0) = \frac{0.75}{2} = 0.375$$
 A

Similarly for steady state the circuit is as shown below

1.5 A

$$i(\infty) = 10 \Omega$$

$$i(\infty) = 10 \Omega$$

$$i(3) = \frac{15}{3} = 0.5 \text{ A}$$

$$T = \frac{L}{R_{eq}} = \frac{15 \# 10^{-3}}{10 + (10 \parallel 10)} = 10^{-3} \sec \theta$$

$$i(t) = A + Be^{-\frac{t}{1\# 10^{-3}}} = A + Be^{-100t}$$
Now

$$i(0) = A + B = 0.375$$
and

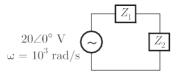
$$i(3) = A = 0.5$$
So,

$$B = 0.375 - 0.5 = -0.125$$
Hence

$$i(t) = 0.5 - 0.125e^{-1000t} \text{ A}$$
Option (A) is correct.

Sol. 26

Circuit is redrawn as shown below



Where,

$$Z_{1} = jwL = j \# 10^{3} \# 20 \# 10^{-3} = 20j$$

$$Z_{2} = R \parallel X_{C}$$

$$X_{C} = \frac{1}{jwC} = \frac{1}{\frac{3}{j\# 10} \# 50 \# 10} = -20j$$

$$Z_2 = \frac{1(-20j)}{1-20j} \qquad \qquad R = 1 \text{ W}$$

Voltage across Z₂

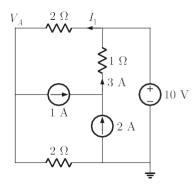
$$V_{Z_2} = \frac{Z_2}{Z_1 + Z_2} : 20 / 0 = \frac{c \frac{-20j}{1 - 20j}}{c^{20j} - \frac{20j}{1 - 20j}} : 20$$
$$= c \frac{(-20j)}{c^{20j} + 400 - 20j} : 20 = -$$

Option (A) is correct.

$$I = \frac{V_{z}}{R} = -\frac{j}{1} = -j A$$

Sol. 27

The circuit can be redrawn as



Applying nodal analysis

Current,

$$\frac{V_A - 10}{2} + 1 + \frac{V_A - 0}{2} = 0$$

$$2V_A - 10 + 2 = 0 = V_4 = 4$$
 V
 $I_1 = \frac{10 - 4}{2} = 3$ A

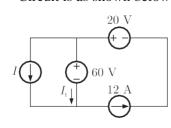
Current from voltage source is

$$I_2 = I_1 - 3 = 0$$

Since current through voltage source is zero, therefore power delivered is zero.

Sol. 28

Option (A) is correct. Circuit is as shown below



Since 60 V source is absorbing power. So, in 60 V source current flows from + to - ve direction

So,
$$I + I_1 = 12$$

 $I = 12 - I_1$

I is always less then 12 A So, only option (A) satisfies this conditions.

Sol. 29

For given network we have

Option (C) is correct.

$${}_{0}V = \frac{(R_{L} || X_{C}) V_{i}}{R + (R_{L} || X_{C})}$$

$$\frac{V_{0}(s)}{V_{i}(s)} = \frac{\frac{R_{L}}{1 + sR_{L}C}}{R + \frac{R_{L}}{1 + sR_{L}C}} = \frac{R_{L}}{R + RR_{L}sC + R_{L}}$$

$$= \frac{R_{L}}{R + RR_{L}sC + R_{L}} = \frac{1}{1 + \frac{R}{R_{L}} + RsC}$$

But we have been given

$$T.F. = \frac{V_0(s)}{V_i(s)} = \frac{1}{2 + sCR}$$

Comparing, we get

$$1 + \frac{R}{R_L} = 2 \qquad \& R_L = R$$

Sol. 30 Option (C) is correct. The energy delivered in 10 minutes is

$$E = \#_{0}^{t} VIdt = I \#_{0}^{t} Vdt = I \# Area$$
$$= 2 \# \frac{1}{2} (10 + 12) \# 600 = 13.2 \text{ kJ}$$

Sol. 31 Option (B) is correct.

From given circuit the load current is

$$I_L = \frac{V}{Z_s + Z_L} = \frac{20 + 0c}{(1 + 2j) + (7 + 4j)} = \frac{20 + 0c}{8 + 6j}$$

= $\frac{1}{5}(8 - 6j) = \frac{20 + 0c}{10 + f} = 2 + -f$ where $f = \tan^{-1}\frac{3}{4}$

The voltage across load is

Option (B) is correct.

 $V_L = I_L Z_L$

The reactive power consumed by load is

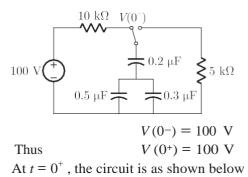
$$P_{r} = V I_{L L}^{*} = I Z_{L L}^{\#} I_{L L}^{*} = Z I_{L | L |}^{2}$$

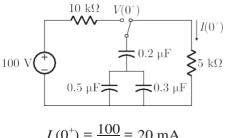
$$= (7 \# 4j) \left| \frac{20 + 0c}{8 + 6j} \right|^{2} = (7 + 4j) = 28 + 16j$$

Thus average power is 28 and reactive power is 16.

Sol. 32

At $t = 0^-$, the circuit is as shown in fig below :





$$(0) = -20$$

5k

At steady state i.e. at t = 3 is I(3) = 0 $i(t) = I(0^+) e^{-\frac{t}{RC_{eq}}} u(t)$

Now

$$C_{eq} = \frac{(0.5m + 0.3m)0.2m}{0.5m + 0.3m + 0.2m} = 0.16 \text{ m F}$$

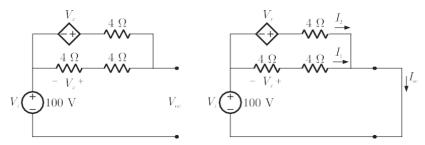
$$\frac{1}{RC_{eq}} = \frac{1}{5 \# 10^3 \# 0.16 \# 10^{-6}} = 1250$$

$$i(t) = 20e^{-1250t}u(t) \text{ mA}$$

Sol. 33

Option (C) is correct.

For P_{max} the load resistance R_L must be equal to the venin resistance R_{eq} i.e. R_L = R_{eq} . The open circuit and short circuit is as shown below



The open circuit voltage is

From fig

$$V_{oc} = 100 \text{ V}$$

$$I_1 = \frac{100}{8} = 12.5 \text{ A}$$

$$V_x = -4 \# 12.5 = -50 \text{ V}$$

$$I_2 = \frac{100 + V_x}{4} = \frac{100 - 50}{4} = 12.5 \text{ A}$$

$$I_{sc} = I_1 + I_2 = 25 \text{ A}$$

$$R_{th} = \frac{V_{oc}}{I_{sc}} = \frac{100}{25} = 4 \text{ W}$$

$$I_{sc} = 25$$

Thus for maximum power transfer $R_L = R_{eq} = 4$ W

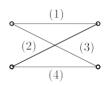
Sol. 34

Option (A) is correct.

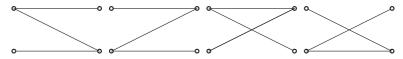
Steady state all transient effect die out and inductor act as short circuits and forced response acts only. It doesn't depend on initial current state. From the given time domain behavior we get that circuit has only R and L in series with V_0 . Thus at steady state

$$i(t)$$
 " $i(3) = \frac{V_0}{R}$

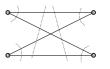
Sol. 35 Option (C) is correct. The given graph is



There can be four possible tree of this graph which are as follows:



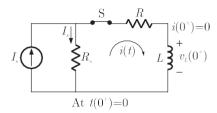
There can be 6 different possible cut-set.





Option (B) is correct.

Initially $i(0^-) = 0$ therefore due to inductor $i(0^+) = 0$. Thus all current I_s will flow in resistor *R* and voltage across resistor will be $I_s R_s$. The voltage across inductor will be equal to voltage across R_s as no current flow through *R*.



Thus

but

$$v_L(0^+) = I_s R_s$$
$$v_L(0^+) = L \frac{di(0^+)}{dt}$$

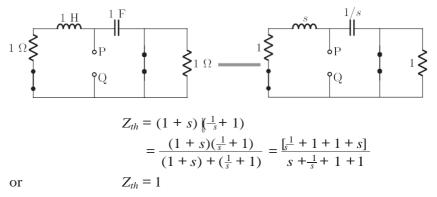
Thus

$$\frac{di(0^{+})}{dt} = \frac{v_L(0^{+})}{L} = \frac{I_s R_s}{L}$$

Sol. 37

Option (A) is correct.

Killing all current source and voltage sources we have,



Alternative :

Here at DC source capacitor act as open circuit and inductor act as short circuit. Thus we can directly calculate thevenin Impedance as 1 W

Option (D) is correct.

$$Z(s) = R \left\| \frac{1}{sC} s \right\|^{L} = \frac{\frac{s}{C}}{\frac{s^{2} + \frac{s}{RC} + \frac{1}{LC}}{\frac{s^{2} + \frac{s}{RC} + \frac{1}{LC}}{\frac{s}{RC}}}$$

We have been given

$$Z(s) = \frac{0.2s}{s^2 + 0.1s + 2}$$

Comparing with given we get

$$\frac{1}{C} = 0.2 \text{ or } C = 5 \text{ F}$$

$$\frac{1}{RC} = 0.1 \text{ or } R = 2 \text{ W}$$

$$\frac{1}{LC} = 2 \text{ or } L = 0.1 \text{ H}$$

Sol. 39

Option (C) is correct. Voltage across capacitor

$$V_{c} = \frac{1}{C} \#^{t}_{0} idt$$

Here C = 1 F and i = 1 A. Therefore

$$V_c = \# dt$$

For $0 \le t \le T$, capacitor will be charged from 0 V

$$V_c = \#_0^t dt = t$$

At t = T, $V_c = T$ Volts

For $T \le t \le 2T$, capacitor will be discharged from *T* volts as

$$V_c = T - \#_T^t dt = 2T - t$$

At t = 2T, $V_c = 0$ volts

For $2T \le t \le 3T$, capacitor will be charged from 0 V

$$V_c = \# dt = t - 2T$$

At t = 3T, $V_c = T$ Volts

For $3T \le t \le 4T$, capacitor will be discharged from *T* Volts

$$V_c = T - \# dt = 4T - t$$

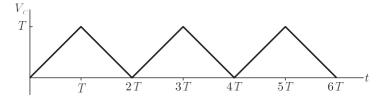
At t = 4T, $V_c = 0$ Volts

For $4T \le t \le 5T$, capacitor will be charged from 0 V

$$V_c = \# dt = t - 4T$$

At t = 5T, $V_c = T$ Volts

Thus the output waveform is



Only option C satisfy this waveform. Option (D) is correct. Writing in transform domain we have $\frac{V_c(s)}{V_s(s)} = \frac{1}{\sqrt{\frac{1}{s}+s+1}} = \frac{1}{(s^{\frac{3}{4}+s+1})}$ Since $V_s(t) = d(t)$ " $V_s(s) = 1$ and $V_c(s) = \frac{1}{(s^2+s+1)}$ or $V_c(s) = \frac{2}{\sqrt{\frac{3}{3}}} \frac{\frac{\sqrt{3}}{2}}{(s+\frac{1}{2})^2 + \frac{3}{4}}$ Taking inverse Laplace transform we have

$$V_t = \frac{2}{\sqrt{3}} e^{-\frac{t}{2}} \operatorname{sin} \operatorname{c} \frac{3}{2} t \, \mathrm{m}$$

Sol. 41

Sol. 40

Let voltage across resistor be v_R

Option (B) is correct.

$$\frac{V_R(s)}{V_S(s)} = \frac{1}{(\frac{1}{s} + s + 1)} = \frac{s}{(s^2 + s + 1)}$$

Since
$$v_s = d(t)$$
 " $V_s(s) = 1$ we get

$$V_R(s) = \frac{s}{(s^2 + s + 1)} = \frac{s}{(s + \frac{1}{2})^2 + \frac{3}{4}}$$
$$= \frac{(s + \frac{1}{2})}{(s + \frac{1}{2})^2 + \frac{3}{4}} - \frac{\frac{1}{2}}{(s + \frac{1}{2})^2 + \frac{3}{4}}$$
$$v_R(t) = e^{-\frac{1}{2}} \cos \frac{3}{2} t - \frac{1}{2} \# \frac{2}{\sqrt{3}} e^{-\frac{1}{2}} \sin \frac{3}{2} t$$

or

$$= e^{-\frac{t}{2}} = \cos\frac{\cancel{2}}{2} \quad t - \frac{1}{\sqrt{3}} \sin\frac{3}{2} t_{G}$$

Sol. 42

Option (C) is correct.

From the problem statement we have

$$\begin{aligned} z_{11} &= \frac{v_1}{i_1} \Big|_{z=0} &= \frac{6}{4} = 1.5 W \\ z_{12} &= \frac{v_1}{i_2} \Big|_{z=0} &= \frac{4.5}{1} = 4.5 W \\ z_{21} &= \frac{v_2}{i_1} \Big|_{i_2=0} &= \frac{6}{1} = 1.5 W \\ z_{22} &= \frac{v_2}{i_2} \Big|_{i_2=0} &= \frac{1.5}{1} = 1.5 W \end{aligned}$$

Thus z -parameter matrix is

Option (A) is correct.

$$=_{z_{21}}^{z_{11}} \sum_{z_{22}}^{z_{12}} G = =_{1.5}^{1.5} \frac{4.5}{1.5} G$$

Sol. 43

From the problem statement we have

$$\begin{array}{c|c} h_{12} = \frac{v_1}{v_2} \\ |_{\tau=0} &= \frac{4.5}{1.5} = 3 \\ h_{22} = \frac{i_2}{v_2} \\ |_{\tau=0} &= \frac{1}{1.5} = 0.67 \end{array}$$

From *z* matrix, we have

$$v_1 = z_{11}i_1 + z_{12}i_2$$

$$v_2 = z_{21}i_1 + z_{22}i_2$$

$$i_2 = -z_{21}i_1 + z_{22}i_2$$

$$i_1 = -1.5 = -1 = h_{21}$$
or
$$i_2 = -i_1$$

Putting in equation for v_1 , we get

$$v_1 = (z_{11} - z_{12}) i_1$$

 $\frac{v_1}{i_1} = h_{11} = z_{11} - z_{12} = 1.5 - 4.5 = -3$

Hence h —parameter will be

$$= \begin{array}{c} h_{11} & h_{12} \\ = & h_{21} & h_{22} \\ \end{array} \mathbf{G} = = \begin{array}{c} -3 & 3 \\ = & -1 & 0.67 \\ \end{array} \mathbf{G}$$

Sol. 44 Option (D) is correct. According to maximum Power Transform Theorem $Z_L = Z_s^* = (R - jX)_s$

Sol. 45 Option (C) is correct.

At w " 3, capacitor acts as short circuited and circuit acts as shown in fig below

$$\begin{array}{c} & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ \end{array} \end{array}$$

At w " 0, capacitor acts as open circuited and circuit look like as shown in fig below

$$\begin{array}{c} & & & \\ & & & & \\ & & & \\ & & & & \\ & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & \\$$

So frequency response of the circuit is as shown in fig and circuit is a Band pass filter.

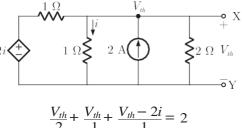
Sol. 46

Option (D) is correct. We know that bandwidth of series *RLC* circuit is $\frac{R}{L}$. Therefore

Bandwidth of filter 1 is $B_1 = \frac{R}{L_1}$ Bandwidth of filter 2 is $B_2 = \frac{R}{L_2} = \frac{R}{L_1/4} = \frac{4R}{L_1}$ Dividing above equation $\frac{B_1}{B_2} = \frac{1}{4}$

Option (D) is correct.

Here V_{th} is voltage across node also. Applying nodal analysis we get



But from circuit
$$i = \frac{V_{th}}{1} = V_{th}$$

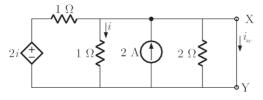
 $V_{th} = 4$ volt

Therefore

$$\frac{V_{th}}{2} + \frac{V_{th}}{1} + \frac{V_{th} - 2V_{th}}{1} = 2$$

or

From the figure shown below it may be easily seen that the short circuit current at terminal X Y is $i_{sc} = 2$ A because i = 0 due to short circuit of 1 W resistor and all current will pass through short circuit.



Therefore

$$R_{th} = \frac{V_{th}}{i_{sc}} = \frac{4}{2}$$

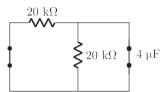
2 W

Sol. 48

Option (A) is correct.
The voltage across capacitor is
At
$$t = 0^+$$
, $V_c(0^+) = 0$
At $t = 3$, $V_C(3) = 5$ V

The equivalent resistance seen by capacitor as shown in fig is

$$R_{ea} = 20 || 20 = 10 k W$$



Time constant of the circuit is

 $T = R_{eq}C = 10k \# 4m = 0.04$ s

Using direct formula

$$V_{c}(t) = V_{C}(\mathbf{3}) - [V_{c}(\mathbf{3}) - V_{c}(0)]e^{-t/T}$$

= $V_{C}(\mathbf{3})(1 - e^{-t/T}) + V_{C}(0)e^{-t/T} = 5(1 - e^{-t/0.04})$
 $V_{c}(t) = 5(1 - e^{-25t})$

or

Now

$$\begin{aligned} &I_C(t) = C \frac{dV_C(t)}{dt} \\ &= 4 \# 10^{-6} \# (-5 \# 25e^{-25t}) = 0.5e^{-25t} \text{ mA} \end{aligned}$$

Option (D) is correct. Sol. 49

Impedance

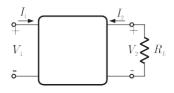
$$= (5 - 3j) (5 + 3j) = \frac{(5 - 3j) \# (5 + 3j)}{5 - 3j + 5 + 3j}$$
$$= \frac{(5)^2 - (3j)^2}{10} = \frac{25 + 9}{10} = 3.4$$

 V_{AB} = Current # Impedance = 5+30**c** # 34 = 17+30**c**

Sol. 50

Option (D) is correct.

The network is shown in figure below.



7	$V_1 = AV_2 - BI_2$	(1)
	I = CV DI	(2)

$$I_1 = CV_2 - DI_2$$
 ...(2)

$$V_2 = -I_2 R_L \qquad \dots (3)$$

From (1) and (2) we get

Thus

Now

and also

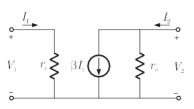
Thus
$$\frac{V_1}{I_1} = \frac{AV_2 - BI_2}{CV_2 - DI_2}$$

Substituting value of V_2 from (3) we get
Input Impedance
$$Z_{in} = \frac{-A \# I_2 R_L - BI_2}{-C \# I_2 R_L - DI_2}$$

or
$$Z_{in} = \frac{AR_L + B}{CR_L + D}$$

Sol. 51

Option (B) is correct. The circuit is as shown below.



Option (B) is correct.

At input port $V_1 = r_e I_1$ $V_2 = r_0(I_2 - bI_1) = -r_0 bI_1 + r_0 I_2$ At output port Comparing standard equation

$$V_1 = z_{11}I_1 + z_{12}I_2$$

$$V_2 = z_{21}I_1 + z_{22}I_2$$

$$z_{12} = 0 \text{ and } z_{21} = -r_0b$$

Sol. 52

For series RC network input impedance is

$$Z_{ins} = \frac{1}{sC} + R = \frac{1 + sRC}{sC}$$

Thus pole is at origin and zero is at $-\frac{1}{2}$ RC For parallel *RC* network input impedance is

$$Z_{in} = \frac{\frac{1}{sC}R}{\frac{1}{sC} + R} = \frac{sC}{1 + sRC}$$

Thus pole is at $\frac{1}{RC}$ and zero is at infinity.

Option (A) is correct. Sol. 53

> $v = \frac{Ldi}{dt}$ We know

Taking Laplace transform we get

$$V(s) = sLI(s) - Li(0^{+})$$

As per given in question $-Li (0^+) = -1 \text{ mV}$ $i (0^+) = \frac{1 \text{ mV}}{2 \text{ mH}} = 0.5 \text{ A}$

Thus

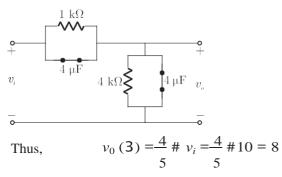
Sol. 54

Option (B) is correct.

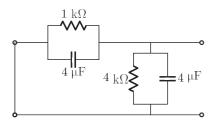
At initial all voltage are zero. So output is also zero.

Thus
$$v_0(0^+) = 0$$

At steady state capacitor act as open circuit.



The equivalent resistance and capacitance can be calculate after killing all source



$$R_{eq} = 1 || 4 = 0.8 \text{ kW}$$

$$C_{eq} = 4 || 1 = 5 \text{ m F}$$

$$T = R_{eq} C_{eq} = 0.8 \text{ kW # 5mF} = 4 \text{ ms}$$

$$v_0(t) = v_0(\mathbf{3}) - [v_0(\mathbf{3}) - v_0(0^+)] e^{-t/T}$$

$$= 8 - (8 - 0) e^{-t/0.004}$$

 $v_0(t) = 8(1 - e^{-t/0.004})$ Volts Option (A) is correct. Sol. 55 Here $Z_2(s) = R_{neg} + Z_1(s)$ $Z_2(s) = R_{neg} + \operatorname{Re} Z_1(s) + j \operatorname{Im} Z_1(s)$ or For $Z_2(s)$ to be positive real, Re $Z_2(s)$ \$ 0 R_{neg} + Re $Z_1(s)$ \$ 0 Thus $\operatorname{Re} Z_1(s)$ \$- R_{neg} or But R_{neg} is negative quantity and $-R_{neg}$ is positive quantity. Therefore Re $Z_1(s)$ \$ | R_{neg} | $|R_{neg}|$ # Re $Z_1(jw)$ or For all *w*. Sol. 56 Option (C) is correct. Transfer function is $\frac{Y(s)}{U(s)} = \frac{\frac{1}{sC}}{R + sL + \frac{1}{c}} = \frac{1}{s^2 LC + scR + 1} = \frac{\frac{1}{LC}}{s^2 + \frac{R}{s}s + \frac{1}{LC}}$ Comparing with $s^2 + 2xw_n s + w_n^2 = 0$ we have $2xw_n = \frac{R}{I}$, Here $w_n = \frac{1}{LC}$ $x = \frac{R}{2L}\sqrt{LC} = \frac{R}{2} \quad \frac{C}{L}$ and Thus For no oscillations, x \$ 1 $\frac{R}{2} \sqrt{\frac{C}{L}}$ \$1 Thus R \$ 2 ± or Option (B) is correct. Sol. 57 For given transformer $\frac{I_2}{I_1} = \frac{V_1}{V_2} = \frac{n}{1}$ $I_1 = \frac{I_2}{n}$ and $V_1 = n V_2$ or Comparing with standard equation $V_1 = AV_2 + BI_2$ $I_1 = CV_2 + DI_2$ $= \frac{A}{C} \frac{B}{D} \mathbf{G} = = \frac{n}{0} \frac{1}{n} \mathbf{G}$ $x = \frac{1}{n}$ Thus Option (B) is correct. Sol. 58 We have L = 1H and $C = \frac{1}{400} # 10^{-6}$ Resonant frequency $f_0 = \frac{1}{2p/LC} = \frac{1}{2p\sqrt{1 \# \frac{1}{400} \# 10^{-6}}}$

$$=\frac{10^3 \# 20}{2p} = \frac{10^4}{p}$$
 Hz

Option (C) is correct. Maximum power will be transferred when $R_L = R_s = 100$ W In this case voltage across R_L is 5 V, therefore $P_{\text{max}} = \frac{V^2}{\mathcal{R}} = \frac{5 \# 5}{100} = 0.25$ W

Sol. 60 Option (C) is correct.

For stability poles and zero interlace on real axis. In *RC* series network the driving point impedance is

$$Z_{ins} = R + \frac{1}{Cs} = \frac{1 + sRC}{sC}$$

Here pole is at origin and zero is at s = -1 / RC, therefore first critical frequency is a pole and last critical frequency is a zero.

For RC parallel network the driving point impedance is

$$Z_{inp} = \frac{\frac{R}{Cs}}{R + \frac{1}{Cs}} = \frac{R}{1 + sRC}$$

Here pole is s = -1 / RC and zero is at 3, therefore first critical frequency is a pole and last critical frequency is a zero.

Sol. 61 Option (A) is correct.

Applying KCL we get

$$i_1(t) + 5 + 0c = 10 + 60c$$

or or

$$i_1(t) = 10+60$$
c $-5+0$ **c** $= 5+5.3j-5$
 $i_1(t) = 5 \quad 3+90$ **c** $= \frac{10}{2} \quad 3+90$ **c**

Sol. 62

Sol. 63

Option (B) is correct.

If $L_1 = j5W$ and $L_3 = j2W$ the mutual induction is subtractive because current enters from dotted terminal of j2W coil and exit from dotted terminal of j5W. If $L_2 = j2W$ and $L_3 = j2W$ the mutual induction is additive because current enters from dotted terminal of both coil.

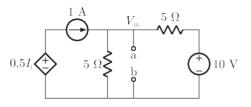
Thus

$$Z = L_1 - M_{13} + L_2 + M_{23} + L_3 - M_{31} + M_{32}$$

= $j5 + j10 + j2 + j10 + j2 - j10 + j10 = j9$

Option (B) is correct.

Open circuit at terminal ab is shown below

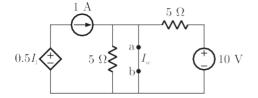


Applying KCL at node we get

$$\frac{V_{ab}}{5} + \frac{V_{ab} - 10}{5} = 1$$
$$V_{ab} = 7.5 = V_{th}$$

or

Short circuit at terminal ab is shown below



Short circuit current from terminal ab is

$$I_{sc} = 1 + \frac{10}{5} = 3 \text{ A}$$

 $R_{th} = \frac{V_{th}}{I_{sc}} = \frac{7.5}{3} = 2.5 \text{ W}$

Here current source being in series with dependent voltage source make it ineffective.

Sol. 64

Option (C) is correct.

Thus

Here
$$V_a = 5$$
 V because $R_1 = R_2$ and total voltage drop is 10 V.
Now $V_b = \frac{R_3}{R_3} \# 10 = \frac{1.1}{4} \# 10 = 5.238$ V $R_3 + R_4$ 2.1 $V = V_a - V_b = 5 - 5.238 = -0.238$ V

Sol. 65

Option (D) is correct.

For *h* parameters we have to write V_1 and I_2 in terms of I_1 and V_2 .

$$V_1 = h_{11}I_1 + h_{12}V_2$$

$$I_2 = h_{21}I_1 + h_{22}V_2$$

Applying KVL at input port

$$V_1 = 10I_1 + V_2$$

Applying KCL at output port

$$\frac{V_2}{20} = I_1 + I_2$$
$$I_2 = -I_1 + \frac{V_2}{20}$$

or

Thus from above equation we get

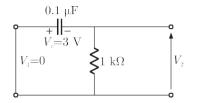
$$= \frac{h_{11}}{h_{12}} \frac{h_{12}}{h_{22}} \mathbf{G} = = \frac{10}{-1} \frac{1}{0.05} \mathbf{G}$$

Sol. 66

Option (B) is correct.

Time constant
$$RC = 0.1 \# 10^{-6} \# 10^{3} = 10^{-4} \sec^{-4}$$

Since time constant *RC* is very small, so steady state will be reached in 2 sec. At t = 2 sec the circuit is as shown in fig.



$$V_c = 3 V$$
$$V_2 = -V_c = -3 V$$

Sol. 67	Option (B) is correct. For a tree there must not be any loop. So a, c, and d don't have any loop. Only b has loop.	
Sol. 68	Option (D) is correct. The sign of <i>M</i> is as per sign of <i>L</i> If current enters or exit the dotted terminals of both coil. The sign of <i>M</i> is opposite of <i>L</i> If current enters in dotted terminal of a coil and exit from the dotted terminal of other coil. Thus $L_{eq} = L_1 + L_2 - 2M$	
Sol. 69	Option (A) is correct. Here $w = 2$ and $V = 1+0c$ $Y = \frac{1}{2} + jwC + \frac{1}{2}$ $= 3 + j2 \# 3 + \frac{1}{j2 \# \frac{1}{4}} = 3 + j4$ $= 5 + \tan^{-1}\frac{4}{3} = 5 + 53.11c$ I = V * Y = (1+0c)(5+53, 1c) = 5+53.1c	
	Thus $i(t) = 5\sin(2t + 53.1c)$	
Sol. 70	Option (A) is correct.	
	$v_i(t) = \sqrt{2}\sin 10^3 t$	
	Here $w = 10^3$ rad and $V_i = \sqrt{2} + 0$ c	
	Now $V_0 = \frac{jwC}{R_j + \frac{1}{V_t}} V_t = \frac{1}{1 + jwCR} V_i$ $= \frac{1}{1 + j \# 10^3 \# 10^{-3}} \sqrt{2} + 0C$ = 1 - 45C / $v_0(t) = \sin(10^3 t - 45C)$	
Sol. 71	Option (C) is correct.	
	Input voltage $v_i(t) = u(t)$	
	Taking Laplace transform $V_i(s) = \frac{1}{s}$	
	Impedance $Z(s) = s + 2$ $I(s) = \frac{V_i(s)}{V_i(s)} = \frac{1}{1}$ or $I(s) = \frac{1}{2} \cdot \frac{1}{s} - \frac{1}{s+2}$	
	or $I(s) = \frac{1}{2} \cdot \frac{1}{s} - \frac{1}{s+2}$	
	Taking inverse Laplace transform	
	$i(t) = \frac{1}{2}(1 - e^{-2t})u(t)$	
	At $t = 0$, $i(t) = 0$	
	At $t = \frac{1}{2}$, $i(t) = 0.31$	
	At $t = 3$, $i(t) = 0.5$	
	Graph (C) satisfies all these conditions.	

Option (D) is correct.

We know that

where

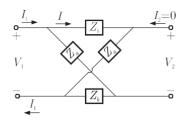
$$V_{1} = z_{11}I_{1} + z_{12}I_{2}$$

$$V_{2} = z_{11}I_{1} + z_{22}I_{2}$$

$$z_{11} = \frac{V_{1}}{I_{1}} \Big|_{I_{2}=0}$$

$$z_{21} = \frac{V_{2}}{I_{1}} \Big|_{I_{1}=0}$$

Consider the given lattice network, when $I_2 = 0$. There is two similar path in the circuit for the current I_1 . So $I = \frac{1}{2}I_1$



For z_{11} applying KVL at input port we get

Thus

$$V_1 = \frac{1}{2}I_1(Z_a + Z_b)$$
$$z_{11} = \frac{1}{2}(Z_a + Z_b)$$

 $V_1 = I \left(Z_a + Z_b \right)$

For Z_{21} applying KVL at output port we get $V_2 = Z \frac{I_1}{I_1} - Z_1 \frac{I_1}{I_1}$

$$V_2 = Z_a \frac{I_1}{2} - Z_b \frac{I_1}{2}$$
$$V_2 = \frac{1}{2} I_1 (Z_a - Z_b)$$

Thus

$$z_{21} = \frac{\frac{2}{1}}{2}(Z_a - Z_b)$$

For this circuit $z_{11} = z_{22}$ and $z_{12} = z_{21}$. Thus $Z_a + Z_b$ $Z_a - Z_b$

$$= z_{21} z_{12} = S = Z_{21} Z_{22} = S = Z_{2a} Z_{2a} Z_{2a} = Z_{2a} Z_{2a$$

Thus

$$=_{z_{21}} g = =_{j-1} 1 + j^{G}$$

Sol. 73

Option (B) is correct.

Applying KVL,

$$v(t) = Ri(t) + \frac{Ldi(t)}{dt} + \frac{1}{C} \#^{3}i(t) dt$$

Taking L.T. on both sides,

$$V(s) = RI(s) + LsI(s) - Li(0^{+}) + \frac{I(s)}{sC} + \frac{v_c(0^{+})}{sC}$$
$$v(t) = u(t) \text{ thus } V(s) = \frac{1}{s}$$

Hence

or

$$\frac{1}{2} = I(s) + sI(s) - 1 + \frac{I(s)}{s} - \frac{1}{s}$$

$$\frac{2}{s} + 1 = \frac{I(s)}{6s^2 + s + 10}$$

$$S = \frac{s}{s^2 + s + 1}$$

= 0 we have

S

Sol. 74

Option (B) is correct.

Option (D) is correct.

Characteristics equation is

$$s^{2} + 20s + 10^{6} = 0$$

aring with $s^{2} + 2xw_{n}s + w^{2} = w_{n} = \overline{10^{6}} = 10^{3}$
 $2xw = 20$

Thus Now

Comp

 $2x = \frac{20}{10^3} = 0.02$ $Q = \frac{10^3}{10^3} = 50$

Sol. 75

$$H(s) = \frac{V_0(s)}{V_i(s)} = \frac{\frac{1}{sC}}{R + sL + \frac{1}{sC}} = \frac{1}{s^2 LC + sCR + 1}$$
$$= \frac{1}{\frac{1}{s^2(10^{-2} \# 10^{-4}) + s(10^{-4} \# 10^4) + 1}}$$
$$= \frac{1}{10^{-6}s^2 + s + 1} = \frac{10^6}{s^2 + 10^6 s + 10^6}$$

0.02

2x

Sol. 76 Option (D) is correct. Impedance of series *RLC* circuit at resonant frequency is minimum, not zero. Actually imaginary part is zero. $Z = R + j wL - \frac{1}{wC} j$ At resonance $wL - \frac{1}{wC} = 0$ and Z = R that is purely resistive. Thus S_1 is false $Q = R \sqrt{\frac{C}{L}}$ Now quality factor $Q = \frac{1}{G} \sqrt{\frac{C}{L}}$ Since $G = \frac{1}{R}$, If G - then Q, provided C and L are constant. Thus S_2 is also false. Sol. 77 Option (B) is correct. Number of loops = b - n + 1= minimum number of equation Number of branches = b = 8Number of nodes = n = 5Minimum number of equation = 8 - 5 + 1 = 4Option (C) is correct. Sol. 78 For maximum power transfer

 $Z_L = Z_S^* = R_s - jX_s$

 $Z_L = 1 - 1j$

Thus

Option (B) is correct.

$$Q = \frac{1}{R} \left/ \frac{L}{C} \right|$$

When *R*, *L* and *C* are doubled,

$$Q' = \frac{1}{2R} \sqrt{\frac{2L}{2C}} = \frac{1}{2R} \qquad \frac{L}{C} = \frac{Q}{2}$$
$$Q' = \frac{100}{2} = 50$$

Thus

Sol. 80 Option (C) is correct.

Applying KVL we get, $\sin t = Ri(t) + L \frac{di(t)}{t} + \frac{1}{t} # i(t) dt$

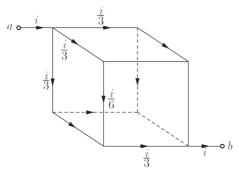
or

Differentiating with respect to t, we get $\cos t \quad \frac{2di(t)}{2} \quad 2d^{2}i(t)$

$$= \frac{2dt(t)}{dt} + \frac{2dt(t)}{dt^2} + i(t)$$

Sol. 81 Option (A) is correct.

For current *i* there is 3 similar path. So current will be divide in three path



so, we get

$$V_{ab} - b\frac{i}{3} # 1 || - b\frac{i}{6} # 1 || - b\frac{1}{3} # 1 || = 0$$
$$\frac{V_{ab}}{i} = R_{eq} = \frac{1}{3} + \frac{1}{6} + \frac{1}{3} = \frac{5}{6} W$$

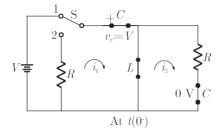
Sol. 82

Data are missing in question as $L_1 \& L_2$ are not given.

Sol. 83 Option (A) is correct.

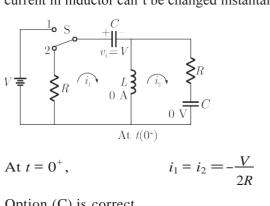
Option () is correct.

At $t = 0^{\circ}$ circuit is in steady state. So inductor act as short circuit and capacitor act as open circuit.



At
$$t = 0^{-}$$
, $i_1(0^{-}) = i_2(0^{-}) = 0$
 $v_c(0^{-}) = V$

At $t = 0^+$ the circuit is as shown in fig. The voltage across capacitor and current in inductor can't be changed instantaneously. Thus



At
$$t = 0^+$$
, $i_1 =$

Option (C) is correct.

or

When switch is in position 2, as shown in fig in question, applying KVL in loop (1), **T** 7 1

$$RI_{1}(s) + \frac{V}{s} + \frac{1}{sC}I_{1}(s) + sL[I_{1}(s) - I_{2}(s)] = 0$$

$$I_{1}(s)_{8}R + \frac{1}{sC} + sL_{B} - I_{2}(s)_{8}SL = \frac{-V}{s}$$

$$z_{11}I_1 + z_{12}I_2 = V_1$$

Applying KVL in loop 2,

$$sL [I_{2}(s) - I_{1}(s)] + RI_{2}(s) + \frac{1}{sC}I_{2}(s) = 0$$

$$Z_{12}I_{1} + Z_{22}I_{2} = V_{2}$$

or

$$-sLI_{1}(s) + gR + sL + \frac{1}{sc}gI_{2}(s) = 0$$

Now comparing with

$$\begin{array}{c} Z_{11} & Z_{12} \\ = \\ Z_{21} & Z_{22} & I_2 \end{array} \begin{array}{c} V_1 \\ G_1 \\ G_2 \\ I_2 \end{array}$$

we get R

$$\begin{array}{ccc} \overset{\mathrm{K}}{\mathrm{S}R} + sL + \frac{1}{sC} & -sL & \overset{\mathrm{W}}{\mathrm{I}}_{1}(s) & - \frac{V}{L} \\ & & \\ \mathrm{S} & \\ & & \\ \mathrm{S} & -sL & R + sL + \frac{1}{sC} \overset{\mathrm{W}}{\mathrm{W}} I_{2}(s)^{\mathsf{G}} = > & \overset{\mathrm{S}}{0} \\ & & \\ & & \\ \end{array}$$

Sol. 85

Option (B) is correct.

Zeros = -3
Pole¹ = -1 + j
Pole² = -1 - j

$$Z(s) = \frac{K(s+3)}{(s+1+j)(s+1-j)} = \frac{K(s+3)}{(s+1)^2 - j^2} = \frac{K(s+3)}{(s+1)^2 + 1}$$
tratement Z(0) + = 3

From problem statement Z(0) = 3Thus $\frac{3K}{2} = 3$ and we get $K = 2^{\pm 0}$ 2(c + 3)

$$Z(s) = \frac{2(s+3)}{s^2 + 2s + 2}$$

Sol. 84

$$v(t) = 10.2 \cos(t + 10c) + 10.5 \cos(2t + 10c) + 10.44442 \exp(44443)$$

Thus we get $w_1 = 1$ and $w_2 = 2$
Now

$$Z_1 = R + jw_1L = 1 + j1$$

$$Z_2 = R + jw_2L = 1 + j2$$

$$i(t) = \frac{v_1(t)}{Z_1} + \frac{v_2(t)}{Z_2} = \frac{10.2 \cos(t + 10c)}{1 + j} + \frac{10.5 \cos(2t + 10c)}{1 + j2}$$

$$= \frac{10.2 \cos(t + 10c)}{1^2 + 2^2 + \tan^{-1}1} + \frac{10.5 \cos(2t + 10c)}{1^2 + 2^2 \tan^{-1}2}$$

$$= \frac{10.2 \cos(t + 10c)}{\sqrt{2} + \tan^{-1}45c} + \frac{10.5 \cos(2t + 10c)}{\sqrt{5} \tan^{-1}2}$$

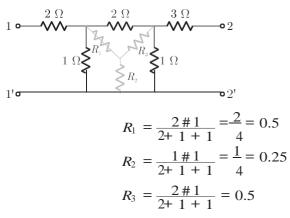
$$i(t) = 10\cos(t - 35c) + 10\cos(2t + 10c - \tan^{-1}2)$$

Sol. 87

Option (A) is correct.

Option (C) is correct.

Using 3- Y conversion

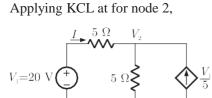


Now the circuit is as shown in figure below.

$$z_{12} = R_3 = 0.25$$

Option (A) is correct.

Sol. 88



$$\frac{V_2}{5} + \frac{V_2 - V_1}{5} = \frac{V_1}{5}$$

or

 $V_2 = V_1 = 20$ V

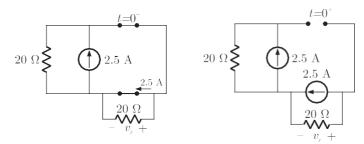
Voltage across dependent current source is 20 thus power delivered by it is

$$PV_2 \# \frac{V_1}{5} = 20 \# \frac{20}{5} = 80 \text{ W}$$

It deliver power because current flows from its + ive terminals.

Option (C) is correct.

When switch was closed, in steady state, $i_L(0) = 2.5 \text{ A}$



At $t = 0^+$, $i_L(0^+) = i_L(0^-) = 2.5$ A and all this current of will pass through 2 W resistor. Thus

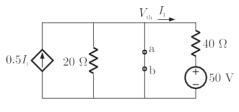
$$V_x = -2.5 \# 20 = -50 \text{ V}$$

Sol. 90

Sol. 89

Option (A) is correct.

For maximum power delivered, R_L must be equal to R_{th} across same terminal.



Applying KCL at Node, we get $0.5I_1 = \frac{V_{th}}{20} + I_1$

or

but

$$I_1 = \frac{V_{th} - 50}{40}$$
$$V_1 = 50$$

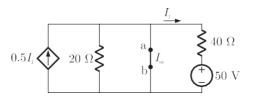
 $V_{th} + 10I_1 = 0$

Thus

or

$$V_{th} + \frac{V_{th} - 50}{4} = 0$$
$$V_{th} = 10 \text{ V}$$

For I_{sc} the circuit is shown in figure below.



but

$$I_1 = -\frac{50}{40} = -1.25 \text{ A}$$

 $I_{sc} = -0.5 \# - 12.5 = 0.625 \text{ A}$

 $I_{sc} = 0.5I_1 - I_1 = -0.5I_1$

١

$$R_{th} = \frac{V_{th}}{I_{sc}} = \frac{10}{0.625} = 16 \text{ W}$$

Sol. 91

 I_P , V_P " Phase current and Phase voltage I_L , V_L " Line current and line voltage

 $V_P = \underbrace{V_L}_{2} \operatorname{m} \operatorname{and} I_P = I_L$ Now

So,

Power =
$$3V_P I_L \cos q$$

also

$$1500 = 3_{\mathsf{C}} \frac{V_L}{3}_{\mathsf{m}}(I_L) \cos \mathfrak{q}$$
$$I_L = \frac{V_L}{\mathsf{C}}_{3Z_L} \mathfrak{m}$$

$$1500 = 3 \frac{V_L}{c_3} \frac{V_L}{mc_3 Z_L} \cos q$$
$$Z_L = \frac{(400)^2 (.844)}{1500} = 90 \text{ W}$$

As power factor is leading

Option (A) is correct.

Option (D) is correct.

 $\cos q = 0.844$ " q = 32.44So,

As phase current leads phase voltage

$$Z_L = 90 + -q = 90 + -32.44$$
c

 $e_0 = 4 \text{ V}$

Sol. 92 Option (C) is correct. Applying KCL, we get

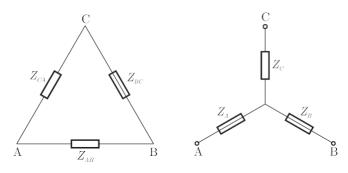
$$\frac{e_0 - 12}{4} + \frac{e_0}{4} + \frac{e_0}{2 + 2} = 0$$

or

Sol. 93

Sol. 94

The star delta circuit is shown as below



7

Here and

$$Z_{AB} = Z_{BC} = Z_{CA} = \overline{3}Z$$

$$Z_{A} = \frac{Z_{AB}Z_{CA}}{Z_{AB} + Z_{BC} + Z_{CA}}$$

$$Z_{B} = \frac{Z_{AB}Z_{BC}}{Z_{AB} + Z_{BC} + Z_{CA}}$$

$$Z_{C} = \frac{Z_{BC}Z_{CA}}{Z_{AB} + Z_{BC} + Z_{CA}}$$

$$Z_{A} = Z_{B} = Z_{C} = \frac{\sqrt{3}Z/3Z}{\sqrt{3}Z + \sqrt{3}Z} = \frac{Z}{3}$$

Now

jwC

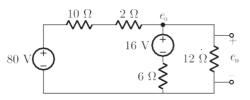
١.

$$= \frac{y_{11}}{y_{21}} \frac{y_{12}}{y_{22}} G = = \frac{y_1 + y_3}{-y_3} \frac{-y_3}{y_2 + y_3} G$$
$$y_{12} = -y_3$$
$$y_{12} = -\frac{1}{20} = -0.05 \text{ mho}$$

Sol. 95

Option (D) is correct.

We apply source conversion the circuit as shown in fig below.



$$\frac{e_0 - 80}{10 + 2} + \frac{e_0}{12} + \frac{e_0 - 16}{6} = 0$$

$$4e_0 = 112$$

$$e_0 = \frac{112}{4} = 28 \text{ V}$$

r

Sol. 96

Sol. 97

Sol. 98

or

~ . •

$$I_{2} = \frac{E_{m} + 0C}{R_{2} + \frac{1}{jwc}} = E_{m} + 0C \frac{jwc}{1 + jwCR_{2}}$$

$$+I_{2} = \frac{+90C}{+ \tan^{-1}wCR_{2}}$$

$$I_{2} = \frac{E_{m}wC}{\sqrt{1 + w^{2}C^{2}R^{2}}} + (90C - \tan^{-1}wCR_{2})$$
At $w = 0$ $I_{2} = 0$
and at $w = 3$, $I_{2} = \frac{E_{m}}{R_{2}}$
Only figure given in option (A) satisfies both conditions.
Option (A) is correct.
 $X_{s} = wL = 10 \text{ W}$
For maximum power transfer
 $R_{L} = \sqrt{R_{s}^{2} + X_{s}^{2}} = -10^{2} + 10^{2} = 14.14 \text{ W}$
Option (C) is correct.
Applying KVL in LHS loop
or
 $E_{1} = \frac{2I_{1}}{11} + \frac{4I_{2}}{11}$
Thus $z_{11} = \frac{6}{11}$
Applying KVL in RHS loop
 $E_{2} = 4 (I_{1} + I_{2}) - 10E_{1}$
 $= 4 (I_{1} + I_{2}) - 10C_{1} \frac{6I_{1}}{14} + \frac{4I_{2}}{4I_{2}}}{m} = -\frac{16I_{1}}{16I_{1}} + \frac{4I_{2}}{4I_{2}}$

11

11

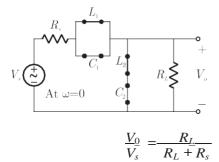
11

11

Thus $z_{21} = -\frac{16}{11}$

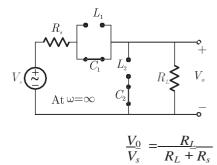
Option (D) is correct.

At w = 0, circuit act as shown in figure below.

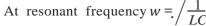


(finite value)

At w = 3, circuit act as shown in figure below:



(finite value)



 $R_t \gtrless V_s = 0$ V

Thus it is a band reject filter.

• _

circuit acts as shown in fig and $V_0 = 0$.

Now

or

$$i_L = e^{at} + e^{bt}$$

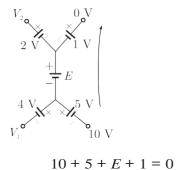
$$V(t) = v_L = L \frac{di_L}{dt} = L \frac{d}{dt} [e^{at} + e^{bt}] = ae^{at} + be^{bt}$$

Sol. 101

Sol. 100

Option (A) is correct. Going from 10 V to 0 V

Option (D) is correct. Applying KCL we get



 $E = -16 \, \mathrm{V}$

Sol. 102 Option (C) is correct.

This is a reciprocal and linear network. So we can apply reciprocity theorem which states "Two loops A & B of a network N and if an ideal voltage source E in loop A produces a current I in loop B, then interchanging positions an identical source in loop B produces the same current in loop A. Since network is linear, principle of homogeneity may be applied and when volt source is doubled, current also doubles.

Now applying reciprocity theorem

$$i = 2 \text{ A for } 10 \text{ V}$$

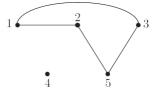
 $V = 10 \text{ V}, i = 2 \text{ A}$
 $V = -20 \text{ V}, i = -4 \text{ A}$

Sol. 103

Option (C) is correct.

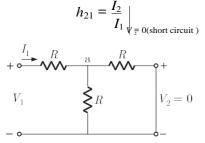
Tree is the set of those branch which does not make any loop and connects all the nodes.

abfg is not a tree because it contains a loopl node (4) is not connected



Sol. 104

Option (A) is correct. For a 2-port network the parameter h_{21} is defined as



Applying node equation at node a we get

$$\frac{V_a - V_1}{R} + \frac{V_a - 0}{R} + \frac{V_a - 0}{R} = 0$$

$$3V_a = V_1 \qquad \& V_a = \frac{V_1}{3}$$

$$\frac{V - V}{V_1 - \frac{V_1}{3}} = 2V_1$$

$$U_1 = \frac{1}{2} = \frac{1}{2$$

Now

and

Thus

$$H_{1} = {}^{1}R^{a} = \frac{3}{R_{1}} = \frac{3}{3R_{1}} = \frac{3}{3R_{1}}$$

$$H_{2} = \frac{0-V}{R^{a}} = \frac{0-V}{R^{a}} = \frac{-V}{3R_{1}} = \frac{-V}{3R_{1}}$$

$$= h_{21} = \frac{-V_{1}/3R}{2V_{1}/3R} = \frac{-1}{2}$$

Sol. 105

Option (A) is correct. Applying node equation at node A

$$\frac{V_{th} - 100(1+j0)}{3} + \frac{V_{th} - 0}{4j} = 0$$

or

$$4jV_{th} - 4j100 + 3V_{th} = 0$$

or

$$V_{th}(3+4j) = 4j100$$
$$V_{th} = \frac{4j100}{3+4j}$$

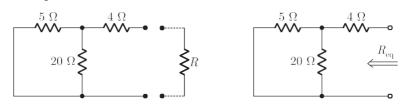
By simplifying

Option (C) is correct.

$$V_{th} = \frac{4j100}{3+4j} \# \frac{3-4j}{3-4j}$$
$$V_{th} = 16j (3-j4)$$

Sol. 106

For maximum power transfer R_L should be equal to R_{Th} at same terminal. so, equivalent Resistor of the circuit is



 $R_{eq} = 5W | 20W + 4W$

$$R_{eq} = \frac{5.20}{5+20} + 4 = 4 + 4 = 8 \text{ W}$$

Sol. 107 Option (D) is correct. Delta to star conversion

$$R_{1} = \frac{R_{ab} R_{ac}}{R_{ab} + R_{ac} + R_{bc}} = \frac{5 \# 30}{5 + 30 + 15} = \frac{150}{50} = 3 \text{ W}$$

$$R_{2} = \frac{R_{ab} R_{bc}}{R_{ab} + R_{ac} + R_{bc}} = \frac{5 \# 15}{5 + 30 + 15} = 1.5 \text{ W}$$

$$R_{3} = \frac{R_{ac} R_{bc}}{R_{ab} + R_{ac} + R_{bc}} = \frac{15 \# 30}{5 + 30 + 15} = 9 \text{ W}$$

Sol. 108 Option (C) is correct.

No. of branches = n + l - 1 = 7 + 5 - 1 = 11

Sol. 109 Option (B) is correct.

In nodal method we sum up all the currents coming & going at the node So it is based on KCL. Furthermore we use ohms law to determine current in individual branch. Thus it is also based on ohms law.

Sol. 110 Option (A) is correct. Superposition theorem is applicable to only linear circuits.

Sol. 111 Option (B) is correct.

Sol. 112 Option (B) is correct. For reciprocal network $y_{12} = y_{21}$ but here $y_{12} = \frac{1}{2}$ $y_{21} = \frac{1}{2}$. Thus circuit is non reciprocal. Furthermore only reciprocal circuit are passive circuit.

Sol. 113 Option (C) is correct. Taking b as reference node and applying KCL at a we get

$$\frac{V_{ab}-1}{2} + \frac{V_{ab}}{2} = 3$$
$$V_{ab} - 1 + V_{ab} = 6$$

or

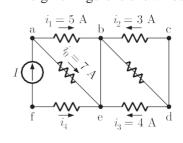
or

$$V_{ab} = \frac{6+1}{2} = 3.5 \text{ V}$$

Sol. 114 Option (A) is correct.

Sol. 115

Option (B) is correct. The given figure is shown below.



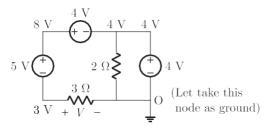
Applying KCL at node a we have

 $I = i_0 + i_1 = 7 + 5 = 12 \text{ A}$ Applying KCL at node f

$$I = -i_4$$
$$i_4 = -12 \text{ amp}$$

Sol. 116

Option (A) is correct.



so

so

V = 3 - 0 = 3 volt

Sol. 117 Option (D) is correct.

Can not determined V without knowing the elements in box.

Sol. 118 Option (A) is correct.

The voltage V is the voltage across voltage source and that is 10 V.

Sol. 119 Option (B) is correct. Voltage across capacitor

$$V_C(t) = V_C(3) + (V_C(0) - V_C(3)) \frac{-t}{e_{RC}}$$

Here $V_C(3) = 10$ V and ($V_C(0) = 6$ V. Thus

 V_R

$$V_C(t) = 10 + (6 - 10) e^{-t} RC = 10 - 4e^{-t} RC = 10 - 4e^{-t}$$

8

Now

$$(t) = 10 - V_C(t) = 10 - 10 + 4e_{RC} = 4e_{RC}^{-t}$$

Energy absorbed by resistor

Option (B) is correct.

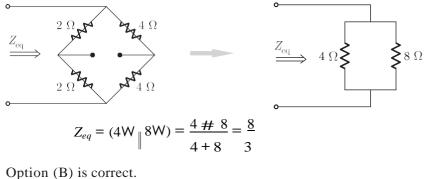
$$E = \#_{0}^{3} \frac{V^{2}(t)}{R} = \#_{0}^{3} \frac{16e^{\frac{-t}{4}}}{4} = \#_{0}^{3} \frac{-t}{4e^{-\frac{t}{4}}} = 16 \text{ J}$$

Sol. 120

It is a balanced whetstone bridge

$$\mathsf{b}\frac{\underline{R}_1}{R} = \frac{\underline{R}_3}{R_4}\,\mathsf{I}$$

so equivalent circuit is



Sol. 121 Option (B) is correct. Current in A_2 , $I_2 = 3$ amp Inductor current can be defined as $I_2 = -3j$ Current in A_3 , $I_3 = 4$ Total current $I_1 = I_2 + I_3 = 4 - 3j$ $|I| = \sqrt{(4)^2 + (3)^2} = 5$ amp

Sol. 122 Option (C) is correct.

For a tree we have (n-1) branches. Links are the branches which from a loop, when connect two nodes of tree.

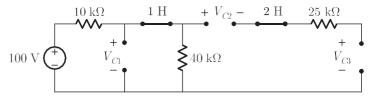
so if total no. of branches = b

No. of links
$$= b - (n - 1) = b - n + 1$$

Total no. of links in equal to total no. of independent loops.

Sol. 123 Option (B) is correct.

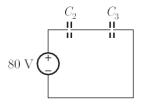
In the steady state condition all capacitors behaves as open circuit & Inductors behaves as short circuits as shown below :



Thus voltage across capacitor C_1 is

$$V_G = \frac{100}{10} + \frac{4}{40} = 80 \text{ V}$$

Now the circuit faced by capacitor C_2 and C_3 can be drawn as below :



Voltage across capacitor C_2 and C_3 are $V_C = 80 \frac{C_3}{C_2 + C_3} = 80 \# \frac{3}{5} = 48$ volt

$$V_{C_3} = 80 \frac{C_2}{C_2 + C_3} = 80 \ \# \frac{2}{5} = 32 \text{ volt}$$
